



CINT2000 Result

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Acer Incorporated
Altos G530 (3.6 GHz Intel Xeon)

SPECint2000 = 1696
SPECint_base2000 = 1692

SPEC license #: 97 Tested by: Acer Incorporated Test date: Aug-2005 Hardware Avail: Aug-2005 Software Avail: Oct-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
164.gzip	1400	117	1193	117	1193				
175.vpr	1400	120	1169	118	1187				
176.gcc	1100	54.8	2006	54.8	2006				
181.mcf	1800	98.1	1834	98.1	1834				
186.crafty	1000	74.9	1335	74.9	1335				
197.parser	1800	123	1458	123	1458				
252.eon	1300	57.8	2249	57.7	2254				
253.perlbmk	1800	87.3	2062	87.2	2065				
254.gap	1100	57.8	1902	57.8	1902				
255.vortex	1900	67.9	2799	67.9	2799				
256.bzip2	1500	120	1247	120	1254				
300.twolf	3000	167	1797	167	1797				

Hardware

CPU: Intel Xeon (3.6 GHz, 2MB L2, 800MHz system bus)
CPU MHz: 3600
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache: 2048KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 4 x 1024MB ECC DDR2-400 SDRAM CL3
Disk Subsystem: 1 x 146GB 10000RPM U320 SCSI HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790) SP1
Compiler: Intel C++ Compiler 8.1 Build 20041019Z
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
SmartHeap Library Version 7.4 from <http://www.microquill.com/>
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon: Approved stdcpp src.alt used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbmk: -fast -Qansi_alias +FDO shlW32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
```



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Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll11 +FDO
300.twolf: -fast -O3 +FDO shlw32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting