



CINT2000 Result

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Advanced Micro Devices
TYAN Tomcat K8E (S2865), AMD Opteron (TM) 154

SPECint2000 = 1956
SPECint_base2000 = 1837

SPEC license #: 49 Tested by: AMD, Austin, TX Test date: Aug-2005 Hardware Avail: Sep-2005 Software Avail: Jun-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
164.gzip	1400	91.0	1539	91.0	1539	[Bar chart showing ratio]			
175.vpr	1400	91.9	1523	89.2	1570	[Bar chart showing ratio]			
176.gcc	1100	53.6	2054	53.1	2073	[Bar chart showing ratio]			
181.mcf	1800	151	1192	123	1466	[Bar chart showing ratio]			
186.crafty	1000	54.6	1832	49.4	2025	[Bar chart showing ratio]			
197.parser	1800	103	1756	102	1760	[Bar chart showing ratio]			
252.eon	1300	49.6	2623	43.1	3018	[Bar chart showing ratio]			
253.perlbnk	1800	95.0	1895	95.0	1895	[Bar chart showing ratio]			
254.gap	1100	56.8	1938	56.8	1938	[Bar chart showing ratio]			
255.vortex	1900	65.3	2910	59.6	3190	[Bar chart showing ratio]			
256.bzip2	1500	102	1476	100	1494	[Bar chart showing ratio]			
300.twolf	3000	156	1929	132	2265	[Bar chart showing ratio]			

Hardware

CPU: AMD Opteron (TM) 154 (939-pin)
CPU MHz: 2800
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip
CPU(s) orderable: 1
Parallel: No
Primary Cache: 64KBI + 64KBD on chip
Secondary Cache: 1024KB (I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 4x512 MB, DDR400 CL2.0
Disk Subsystem: SATA, Western Digital WD740GD, 10000 rpm
Other Hardware: None

Software

Operating System: Microsoft Windows XP Pro SP2
Compiler: Intel C++ 8.0 build 20040415Z for IA32
Microsoft Visual Studio .NET 7.0.9466 (libraries)
MicroQuill SmartHeap Library 7.0
File System: NTFS
System State: default

Notes/Tuning Information

shlw32M.lib is the SmartHeap library V7.0 from MicroQuill www.microquill.com

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability:

176.gcc: -Dalloca=_alloca /F10000000

186.crafty: -DNT_i386

253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT

254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Baseline C: -fast -arch:SSE2 -Oi- +FDO

Baseline C++: -fast -arch:SSE2 -GX -GR

Peak Tuning:

164.gzip: basepeak=yes

175.vpr: -fast -arch:SSE2 +FDO

-Qoption,c,-ip_ninl_max_stats=2000,-ip_ninl_max_total_stats=4500

176.gcc: -fast -arch:SSE2 +FDO -Oi- -Qunroll3

181.mcf: -fast -QaxN +FDO

186.crafty: -fast -arch:SSE2 +FDO

197.parser: -arch:SSE2 +FDO -Oi- -Qipo

252.eon: -fast -arch:SSE2 +FDO -Qansi_alias

-Qoption,c,-ip_ninl_max_stats=2000,-ip_ninl_max_total_stats=4500

253.perlbnk: basepeak=yes

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Notes/Tuning Information (Continued)

```
254.gap:      basepeak=yes
255.vortex:   -fast -arch:SSE +FDO -Oi- shlw32M.lib
              -Qoption,c,-ip_ninl_max_stats=2000,-ip_ninl_max_total_stats=4500
256.bzip2:    -fast -Qunroll2
300.twolf:    -fast -arch:SSE2 +FDO -Qunroll3 shlw32M.lib -Qansi_alias
```

The tested system can be assembled using a standard ATX case and an Antec True 550 Watt EPS12V power supply.
All memory slots were populated with Corsair CMX512-3200XL.
Memory timings manually set in BIOS: CAS=2, TRCD=2, TRAS=5, TRP=2
BIOS V2.00b