# CINT2000 Result

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## Acer Incorporated

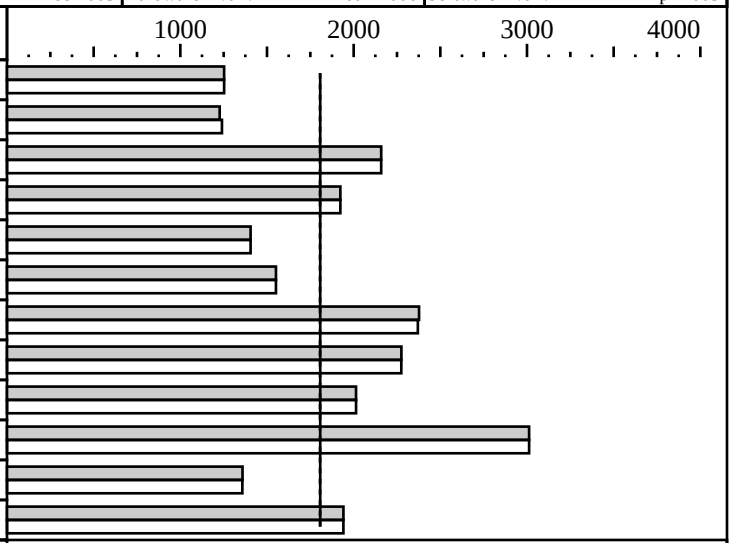
Acer Altos R510 Mk2 (3.8 GHz Intel Xeon processor, 2MB L2, 800MHz system bus)

SPECint2000 = 1807

SPECint_base2000 = 1806

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Dec-2005 | Hardware Avail: Jan-2006 | Software Avail: Apr-2005

| Benchmark | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio |
|---|---|---|---|---|---|
| 164.gzip | 1400 | 112 | 1253 | 112 | 1253 |
| 175.vpr | 1400 | 114 | 1227 | 113 | 1240 |
| 176.gcc | 1100 | 51.0 | 2159 | 51.0 | 2159 |
| 181.mcf | 1800 | 93.6 | 1923 | 93.6 | 1923 |
| 186.crafty | 1000 | 71.2 | 1405 | 71.2 | 1405 |
| 197.parser | 1800 | 116 | 1552 | 116 | 1552 |
| 252.eon | 1300 | 54.7 | 2377 | 54.8 | 2370 |
| 253.perlbmk | 1800 | 79.1 | 2275 | 79.1 | 2275 |
| 254.gap | 1100 | 54.6 | 2014 | 54.6 | 2014 |
| 255.vortex | 1900 | 63.1 | 3012 | 63.1 | 3012 |
| 256.bzip2 | 1500 | 110 | 1359 | 110 | 1358 |
| 300.twolf | 3000 | 155 | 1941 | 155 | 1941 |




### Hardware

| | |
|---|---|
| CPU: | Intel Xeon (3.8 GHz, 2MB L2, 800MHz system bus) |
| CPU MHz: | 3800 |
| FPU: | Integrated |
| CPU(s) enabled: | 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology Disabled) |
| CPU(s) orderable: | 1,2 |
| Parallel: | No |
| Primary Cache: | 12KB(I) micro-ops + 16KB(D) on chip |
| Secondary Cache: | 2048KB(I+D) on chip |
| L3 Cache: | N/A |
| Other Cache: | N/A |
| Memory: | 6 x 512MB ECC DDR2-400 SDRAM |
| Disk Subsystem: | 1 x 160GB 7200RPM SATA HDD |
| Other Hardware: | |

### Software

| | |
|---|---|
| Operating System: | Microsoft Windows Server 2003 Enterprise Edition (Build 3790) |
| Compiler: | Intel C++ Compiler 9.0 Build 20050430Z for IA32<br>Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)<br>shlW32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/ |
| File System: | NTFS |
| System State: | Default |

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
252.eon:   Approved stdcpp src.alt used
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     basepeak=yes
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   basepeak=yes
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
```

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## Notes/Tuning Information (Continued)

```
256.bzip2:   -fast -Oa -Qunroll1 +FDO
300.twolf:    -fast  +FDO shlW32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at http://www.acer.com
```