



# CFP2000 Result

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**Acer Incorporated**  
Acer Altos R910 (3.0 GHz Intel Xeon Processor 7041)

SPECfp\_rate2000 = **58.0**  
SPECfp\_rate\_base2000 = **58.0**

SPEC license #: 97 Tested by: Acer Incorporated Test date: May-2006 Hardware Avail: Jun-2006 Software Avail: Nov-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	8	207	71.7	8	207	71.8
171.swim	8	804	35.8	8	802	35.9
172.mgrid	8	497	33.6	8	497	33.6
173.applu	8	552	35.3	8	552	35.3
177.mesa	8	119	109	8	119	109
178.galgel	8	145	186	8	146	184
179.art	8	263	91.8	8	263	91.8
183.quake	8	291	41.5	8	291	41.5
187.facerec	8	289	61.0	8	289	61.0
188.amp	8	361	56.6	8	356	57.3
189.lucas	8	513	36.2	8	513	36.2
191.fma3d	8	454	42.9	8	454	42.9
200.sixtrack	8	193	53.0	8	194	52.7
301.apsi	8	331	72.9	8	334	72.1

### Hardware

CPU: Intel Xeon Processor 7041 (3.0 GHz, 2x2MB L2, 800MHz system bus)  
CPU MHz: 3000  
FPU: Integrated  
CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology disabled)  
CPU(s) orderable: 1,2,3,4  
Parallel: No  
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip, per core  
Secondary Cache: 2048KB(I+D) on chip, per core  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 16 x 1024MB ECC DDR2-400 SDRAM  
Disk Subsystem: 1 x 73GB 10000RPM U320 SCSI HDD  
Other Hardware:

### Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1  
Compiler: Intel C++ 9.0 Build 20051130Z for IA32 and Fortran Compiler 9.0 Build 20051130Z for IA32  
Microsoft Visual Studio .Net 2003 (7.1.5091, for libraries)  
shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1= -Qprof_gen PASS2=-Qprof_use
Base tuning for all Fortran Programs : -fast -Qansi_alias +FDO
Base tuning for all C Programs: -fast +FDO shlw32M.lib
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise: -fast -Qansi_alias +FDO
171.swim: -fast -Qansi_alias +FDO
172.mgrid: -fast -Qansi_alias +FDO
173.applu: basepeak=yes
177.mesa: basepeak=yes
178.galgel: -fast -Qansi_alias +FDO
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd +FDO shlw32M.lib
187.facerec: basepeak=yes
188.amp: -Oa -QxP
189.lucas: -fast -Qprefetch- +FDO
```



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## Notes/Tuning Information (Continued)

191.fma3d: basepeak=yes

200.sixtrack: -Qipo -QxP +FDO

301.apsi: -fast +FDO

Enable hardware prefetching and adjacent cache line prefetch BIOS setting