CINT2000 Result

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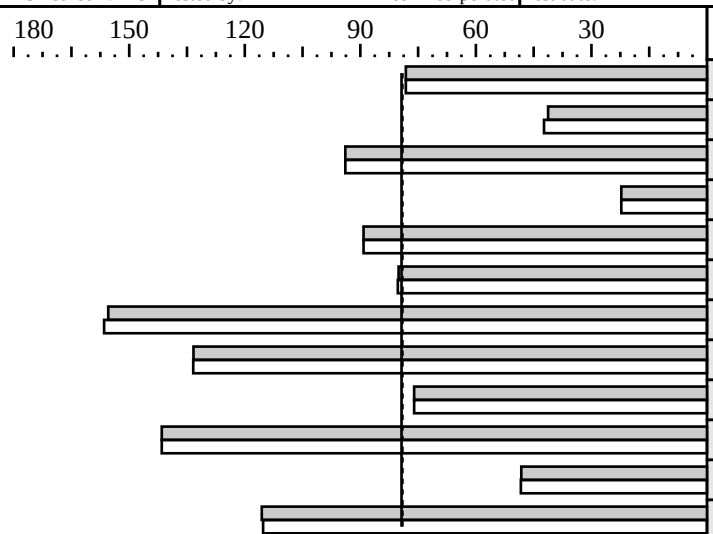
Acer Incorporated

Acer Altos R910 (2.66 GHz Intel Xeon Processor 7020)

SPECint_rate2000 = 79.3

SPECint_rate_base2000 = 79.1

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2006 | Hardware Avail: Jun-2006 | Software Avail: Nov-2005



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	166	78.2	8	166	78.2
175.vpr	8	315	41.3	8	307	42.3
176.gcc	8	109	93.9	8	109	93.9
181.mcf	8	751	22.2	8	751	22.2
186.crafty	8	104	89.2	8	104	89.2
197.parser	8	209	80.1	8	208	80.3
252.eon	8	77.6	155	8	77.1	157
253.perlbmk	8	125	133	8	125	133
254.gap	8	134	76.0	8	134	76.0
255.vortex	8	125	142	8	125	142
256.bzip2	8	289	48.2	8	288	48.3
300.twolf	8	241	116	8	242	115

Hardware

CPU:	Intel Xeon Processor 7020 (2.66 GHz, 2x1MB L2, 667MHz system bus)
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2,3,4
Parallel:	No
Primary Cache:	12KB(I) micro-ops + 16KB(D) on chip, per core
Secondary Cache:	1024KB(I+D) on chip, per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	16 x 1024MB ECC DDR2-400 SDRAM
Disk Subsystem:	1 x 73GB 10000RPM U320 SCSI HDD
Other Hardware:	

Software

Operating System:	Microsoft Windows Server 2003 Enterprise Edition (Build 3790), Service Pack 1
Compiler:	Intel C++ Compiler 9.0 Build 20051130Z for IA32 Microsoft Visual Studio .Net 2003 (7.1.3091, for libraries) shlW32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast +FDO shlW32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:     basepeak=yes
175.vpr:      -fast -Qansi_alias +FDO
176.gcc:      basepeak=yes
181.mcf:      basepeak=yes
186.crafty:   basepeak=yes
197.parser:   -fast -Qansi_alias  +FDO
252.eon:      -fast +FDO
253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
254.gap:      basepeak=yes
255.vortex    basepeak=yes
256.bzip2:   -fast -Oa -Qunroll1 +FDO
```

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Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlW32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting