CINT2000 Result

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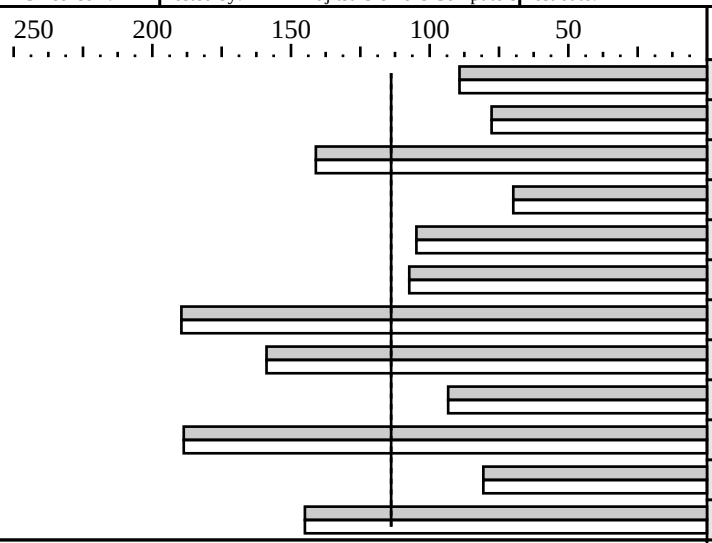
Fujitsu Siemens Computers

PRIMERGY TX600 S3, Intel Xeon processor 7041, 3.0 GHz

SPECint_rate2000 = 114

SPECint_rate_base2000 = 114

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Jun-2006 | Hardware Avail: May-2006 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	146	89.2	8	146	89.2
175.vpr	8	167	77.7	8	167	77.7
176.gcc	8	72.4	141	8	72.4	141
181.mcf	8	239	69.9	8	239	69.9
186.crafty	8	88.6	105	8	88.6	105
197.parser	8	156	107	8	156	107
252.eon	8	63.7	190	8	63.7	190
253.perlbmk	8	105	159	8	105	159
254.gap	8	109	93.3	8	109	93.3
255.vortex	8	93.5	189	8	93.5	189
256.bzip2	8	173	80.6	8	173	80.6
300.twolf	8	192	145	8	192	145

Hardware

CPU:	Intel Xeon processor 7041 (3.0 GHz, 2x2MB L2, 800 MHz system bus)
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology disabled)
CPU(s) orderable:	1,2,4
Parallel:	No
Primary Cache:	12k micro-ops I + 16KBD on chip, per core
Secondary Cache:	2048KB(I+D) on chip, per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	16x1GB DDRII-RAM PC2-3200R (CAS 3-3-3)
Disk Subsystem:	Fujitsu MAS3367NC (SCSI, 15krpm, 36GB)
Other Hardware:	none

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler 9.1 Build 20060323Z (for 32-bit applications), Microsoft Visual Studio .Net 2003 7.1.3088 (for libraries), SmartHeap Library Version 8.0 from http://www.microquill.com
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
GENERAL
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use

Portability flags
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafty:  -DNT_i386
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags
   for C programs:   -fast +FDO shlW32M.lib
   for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib

Peak tuning flags
   same as baseline (basepeak=true set globally)

The system bus runs at 800 MHz

This result was measured with 32-bit binaries using the 32-bit version of
the operating system.
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Notes/Tuning Information (Continued)

BIOS Configuration:
Hardware Prefetch = Enable, Adjacent Sector Prefetch = Disable

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