



CINT2000 Result

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Hewlett-Packard Company
ProLiant DL360 G5 (2.66 GHz, Intel Xeon processor 5150)

SPECint_rate2000 = **56.6**
SPECint_rate_base2000 = **56.3**

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Aug-2006 Hardware Avail: May-2006 Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	86.9	37.4	2	86.6	37.5
175.vpr	2	82.5	39.4	2	78.8	41.2
176.gcc	2	42.7	59.7	2	42.7	59.7
181.mcf	2	78.4	53.2	2	78.5	53.2
186.crafty	2	41.1	56.5	2	41.1	56.5
197.parser	2	90.9	46.0	2	90.3	46.2
252.eon	2	37.4	80.7	2	37.4	80.7
253.perlbnk	2	58.6	71.2	2	58.4	71.5
254.gap	2	45.4	56.3	2	45.4	56.3
255.vortex	2	48.6	90.6	2	48.6	90.8
256.bzip2	2	85.9	40.5	2	85.9	40.5
300.twolf	2	98.3	70.8	2	98.2	70.9

Hardware

CPU: Intel Xeon processor 5150 (2.66 GHz, 4 MB L2 shared, 1333 MHz bus)
CPU MHz: 2660
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1,2 chips
Parallel: No
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per core
L3 Cache: N/A
Other Cache: N/A
Memory: 8 GB (8x1024 MB PC2-5300F)
Disk Subsystem: 1x36 GB 10 K SAS
Other Hardware:

Software

Operating System: Windows Server 2003 Enterprise SP1
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)
Microsoft Visual Studio .NET 7.0.9466 (for libraries)
MicroQuill Smartheap Library 7.0
File System: NTFS
System State: Default

Notes/Tuning Information

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=_alloca /F10000000

186.crafty: -DNT_i386

253.perlbnk: -DSPEC_CPU2000_NTOS -DPERL DLL /MT

254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



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Notes/Tuning Information (Continued)

256.bzip2: basepeak=1
300.twolf: -fast +FDO shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch disabled