CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Hewlett-Packard Company

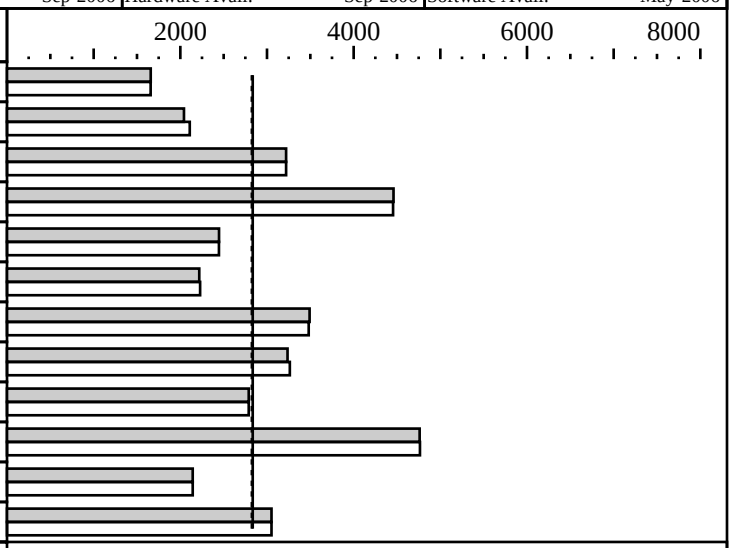
ProLiant ML310 G4 (2.66 GHz, Intel Xeon processor 3070)

SPECint2000 = 2835

SPECint_base2000 = 2826

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Sep-2006 | Hardware Avail: Sep-2006 | Software Avail: May-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	84.4	1658	84.5	1657
175.vpr	1400	68.6	2042	66.4	2109
176.gcc	1100	34.2	3220	34.2	3220
181.mcf	1800	40.4	4460	40.4	4455
186.crafty	1000	40.9	2446	40.9	2446
197.parser	1800	81.1	2218	80.8	2228
252.eon	1300	37.2	3491	37.3	3481
253.perlbmk	1800	55.6	3237	55.2	3263
254.gap	1100	39.4	2789	39.4	2789
255.vortex	1900	39.9	4761	39.9	4765
256.bzip2	1500	70.0	2143	70.0	2143
300.twolf	3000	98.3	3052	98.3	3052



Hardware

CPU:	Intel Xeon processor 3070 (2.66GHz, 4MB L2 shared, 1066 MHz bus)
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Parallel:	No
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4 GB (2x2 GB PC2-5300E)
Disk Subsystem:	1x80 GB SATA150
Other Hardware:	

Software

Operating System:	Windows Server 2003 Enterprise SP1
Compiler:	Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z) Intel Fortran Compiler for 32-bit applications, (Version 9.1 Build 20060323Z) Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast         +FDO  ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO  ONESTEP=yes
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafty: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:     -fast               +FDO  ONESTEP=yes
  175.vpr:      -fast               +FDO  ONESTEP=yes
  176.gcc:      basepeak=1
  181.mcf:      -fast               +FDO  ONESTEP=yes shlw32M.lib
  186.crafty:   -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  197.parser:   -fast               +FDO  ONESTEP=yes
  252.eon:      -fast               +FDO
  253.perlbmk:  -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  254.gap:      -fast               +FDO  ONESTEP=yes
  255.vortex:   -fast               +FDO  ONESTEP=yes shlw32M.lib
  256.bzip2:    basepeak=1
```

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
  300.twolf:    -fast                +FDO                shlw32M.lib
BIOS Configuration Notes
   Power Regulator set to Static High Performance Mode
```