CINT2000 Result

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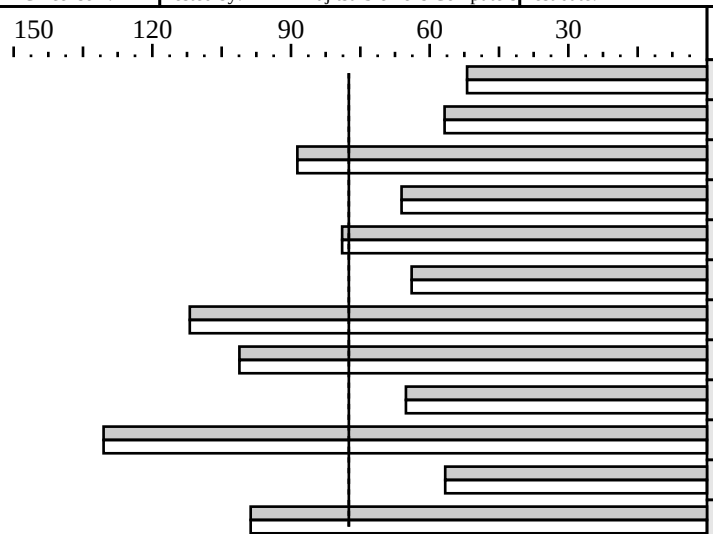
Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor E5320, 1.86 GHz

SPECint_rate2000 = 77.5

SPECint_rate_base2000 = 77.5

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Sep-2006 | Hardware Avail: Nov-2006 | Software Avail: May-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	125	51.9	4	125	51.9
175.vpr	4	114	56.8	4	114	56.8
176.gcc	4	57.6	88.6	4	57.6	88.6
181.mcf	4	126	66.1	4	126	66.1
186.crafty	4	58.8	78.9	4	58.8	78.9
197.parser	4	131	63.9	4	131	63.9
252.eon	4	53.9	112	4	53.9	112
253.perlbmk	4	82.6	101	4	82.6	101
254.gap	4	78.4	65.1	4	78.4	65.1
255.vortex	4	67.5	131	4	67.5	131
256.bzip2	4	123	56.6	4	123	56.6
300.twolf	4	141	98.7	4	141	98.7

Hardware

CPU:	Intel Xeon processor E5320 (1.86 GHz, 2x4MB L2, 1066 MHz system bus)
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x1GB DDR2-RAM PC2-4200F (CAS 4-4-4)
Disk Subsystem:	Seagate ST373454SS (SAS, 15.4krpm, 73GB)
Other Hardware:	none

Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler 9.1 Build 20060323Z (for 32-bit applications), Microsoft Visual Studio .Net 2003 7.1.3088 (for libraries), SmartHeap Library Version 8.0 from http://www.microquill.com
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
GENERAL
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use

Portability flags
   176.gcc:     -Dalloca=_alloca /F10000000
   186.crafty:  -DNT_i386
   253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
   254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags
   for C programs:   -fast +FDO shlW32M.lib
   for C++ programs: -fast -Qcxx_features +FDO shlW32M.lib

Peak tuning flags
   same as baseline (basepeak=true set globally)

The system bus runs at 1066 MHz

This result was measured with 32-bit binaries using the 32-bit version of
the operating system.
```

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Notes/Tuning Information (Continued)

BIOS configuration:
Adjacent Sector Prefetch = disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY TX300 S3 and the PRIMERGY RX300 S3 are electronically equivalent.

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