CINT2000 Result

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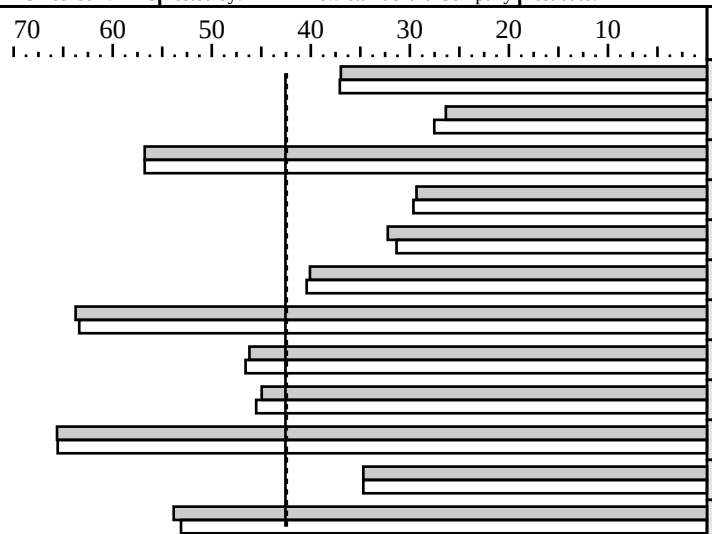
Hewlett-Packard Company

ProLiant DL140 G3 (3.73 GHz, Intel Xeon processor 5080)

SPECint_rate2000 = 42.6

SPECint_rate_base2000 = 42.4

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Nov-2006 | Hardware Avail: Jun-2006 | Software Avail: Oct-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	176	37.0	4	175	37.1
175.vpr	4	246	26.4	4	236	27.5
176.gcc	4	89.9	56.8	4	89.9	56.8
181.mcf	4	285	29.3	4	282	29.6
186.crafty	4	144	32.2	4	148	31.3
197.parser	4	208	40.1	4	207	40.4
252.eon	4	94.6	63.7	4	95.2	63.4
253.perlbmk	4	181	46.2	4	179	46.6
254.gap	4	114	45.0	4	112	45.5
255.vortex	4	134	65.6	4	134	65.6
256.bzip2	4	201	34.7	4	201	34.7
300.twolf	4	259	53.8	4	262	53.1

Hardware

CPU:	Intel Xeon processor 5080 (3.73GHz, 2x2MB L2, 1066 MHz system bus)
CPU MHz:	3730
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip (Hyper-Threading Technology enabled)
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per core
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8 GB (8 x 1 GB PC2-5300F)
Disk Subsystem:	2 x 36 GB 15 K SAS
Other Hardware:	

Software

Operating System:	Windows Server 2003 Enterprise SP1
Compiler:	Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 8.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -fast         +FDO  ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafty: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:     -fast               +FDO  ONESTEP=yes
  175.vpr:      -fast               +FDO  ONESTEP=yes
  176.gcc:      basepeak=1
  181.mcf:      -fast               +FDO  ONESTEP=yes shlw32M.lib
  186.crafty:   -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  197.parser:   -fast               +FDO  ONESTEP=yes
  252.eon:      -fast               +FDO
  253.perlbmk:  -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  254.gap:      -fast               +FDO  ONESTEP=yes
  255.vortex:   -fast               +FDO  ONESTEP=yes shlw32M.lib
  256.bzip2:    basepeak=1
```

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Notes/Tuning Information (Continued)

```
  300.twolf:    -fast                +FDO                shlw32M.lib
BIOS Configuration Notes
  Adjacent Cache Line Prefetch Disabled
  Hyper-Threading Technology enabled
```