CFP2000 Result

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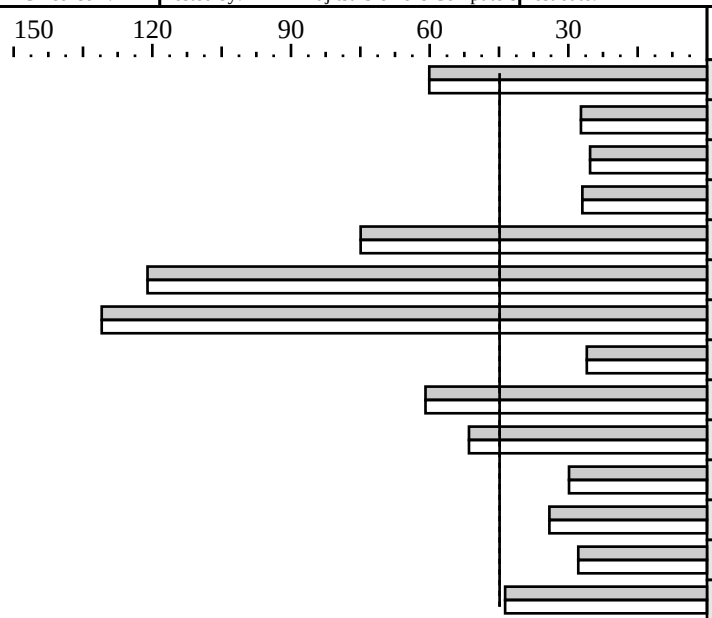
Fujitsu Siemens Computers

PRIMERGY BX620 S3, Intel Xeon processor E5310, 1.60 GHz

SPECfp_rate2000 = 44.9

SPECfp_rate_base2000 = 44.9

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Nov-2006 | Hardware Avail: Nov-2006 | Software Avail: Aug-2006



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	124	60.1	4	124	60.1
171.swim	4	527	27.3	4	527	27.3
172.mgrid	4	330	25.3	4	330	25.3
173.applu	4	361	27.0	4	361	27.0
177.mesa	4	86.7	74.9	4	86.7	74.9
178.galgel	4	111	121	4	111	121
179.art	4	92.1	131	4	92.1	131
183.equake	4	232	26.0	4	232	26.0
187.facerec	4	145	60.9	4	145	60.9
188.ammp	4	198	51.5	4	198	51.5
189.lucas	4	311	29.9	4	311	29.9
191.fma3d	4	286	34.1	4	286	34.1
200.sixtrack	4	183	27.8	4	183	27.8
301.apsi	4	276	43.7	4	276	43.7

Hardware

CPU:	Intel Xeon processor E5310 (1.60 GHz, 2x4MB L2, 1066 MHz system bus)
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Parallel:	No
Primary Cache:	32KB(I) + 32KB(D) on chip, per core
Secondary Cache:	8MB(I+D) on chip, per chip (4MB shared per 2 cores)
L3 Cache:	N/A
Other Cache:	N/A
Memory:	8x1GB DDR2-RAM PC2-5300F (CAS 5-5-5)
Disk Subsystem:	Seagate ST936701SS (SAS, 10 krpm, 36GB)
Other Hardware:	none

Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10 Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ and Fortran Compiler 9.1 for EM64T Build 20060816 (for 64-bit applications)
File System:	reiserfs
System State:	Multi-user run level 3

Notes/Tuning Information

```
GENERAL
+FDO implies feedback-directed optimization
PASS1: -prof_gen  PASS2: -prof_use

Optimization flags
ONESTEP=yes set for all benchmarks

Portability flags
-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

Base tuning flags
   for Fortran and C programs: -fast +FDO

Peak tuning flags
   same as baseline (basepeak=true set globally)
```

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Notes/Tuning Information (Continued)

The system bus runs at 1066 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

BIOS Configuration:
Adjacent Sector Prefetch = Disable

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