



CFP2000 Result

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Dell

PowerEdge 860 (Intel Xeon processor X3220, 2.40GHz)

SPECfp_rate2000 = 60.9

SPECfp_rate_base2000 = 60.9

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Jan-2007 | Hardware Avail: Jan-2007 | Software Avail: May-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	94.0	79.0	4	94.0	79.0
171.swim	4	426	33.7	4	426	33.7
172.mgrid	4	256	32.6	4	256	32.6
173.applu	4	292	33.4	4	292	33.4
177.mesa	4	63.7	102	4	63.7	102
178.galgel	4	77.3	174	4	77.3	174
179.art	4	71.2	170	4	71.2	170
183.equake	4	124	48.5	4	124	48.5
187.facerec	4	111	79.7	4	111	79.7
188.amp	4	161	63.4	4	161	63.4
189.lucas	4	258	35.9	4	258	35.9
191.fma3d	4	227	43.0	4	227	43.0
200.sixtrack	4	117	43.7	4	117	43.7
301.apsi	4	181	66.8	4	181	66.8

Hardware

CPU: Intel Xeon processor X3220 (1066MHz system bus)
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1
 Parallel: No
 Primary Cache: 32KB(I) + 32KB(D) on chip per core
 Secondary Cache: 8 MB I+D on chip (4 MB shared per 2 cores)
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 4 x 1GB DDR2-667 Dual Ranked SDRAM
 Disk Subsystem: 1 x 73GB SAS 10000 RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Standard Edition
 Compiler: Intel C++ Compiler 9.1 for IA32(20060519Z)
 Intel Fortran Compiler 9.1 for IA32(20060519Z)
 Microsoft Visual Studio .NET 2003(7.1.3088)
 MicroQuill SmartHeap Library 8.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

GENERAL

ONESTEP=yes
 +FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

PORTABILITY

178.galgel: -FI /F32000000

BASE TUNING

FORTRAN: -fast -Qansi_alias +FDO
 177.mesa: -fast +FDO shlW32M.lib
 179.art: -fast +FDO shlW32M.lib
 183.equake: -fast +FDO shlW32M.lib
 188.amp: -fast +FDO shlW32M.lib

PEAK TUNING

168.wupwise: basepeak=yes
 171.swim: basepeak=yes
 172.mgrid: basepeak=yes
 173.applu: basepeak=yes
 177.mesa: basepeak=yes



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Notes/Tuning Information (Continued)

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178.galgel: basepeak=yes
179.art: basepeak=yes
183.quake: -fast -Oa -Qrcd +FDO shlW32M.lib
187.facerec: -fast -Qunroll11 -Qscalar_rep- +FDO
188.amp: -fast -Oa shlW32M.lib
189.lucas: basepeak=yes
191.fma3d: basepeak=yes
200.sixtrack: -fast +FDO
301.apsi: basepeak=yes
Adjacent Sector Prefetch disabled in BIOS, default enabled
```