



CINT2000 Result

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IBM Corporation
RISC System/6000 H70 (1 CPU)

SPECint2000 = --
SPECint_base2000 = 168

SPEC license #: 11 | Tested by: IBM, Austin, TX | Test date: Nov-1999 | Hardware Avail: Oct-1998 | Software Avail: Nov-1999

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	1043	134			
175.vpr	1400	666	210			
176.gcc	1100	608	181			
181.mcf	1800	539	334			
186.crafty	1000	632	158			
197.parser	1800	1509	119			
252.eon	1300	954	136			
253.perlbmk	1800	1456	124			
254.gap	1100	842	131			
255.vortex	1900	1101	173			
256.bzip2	1500	894	168			
300.twolf	3000	1185	253			

Hardware

CPU: RS64 II
CPU MHz: 340
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip
CPU(s) orderable: 4
Parallel: No
Primary Cache: 64KBI+64KBD (on chip)
Secondary Cache: 4MB unified (off chip)
L3 Cache: None
Other Cache: None
Memory: 1GB
Disk Subsystem: 1x9.1GB, 2x4.5GB, 4x2.2GB Fast SCSI
Other Hardware: None

Software

Operating System: AIX 4.3.2
Compiler: IBM C for AIX 5.0
IBM C++ for AIX 3.6.6.1
File System: AIX/JFS
System State: Multi-user

Notes/Tuning Information

Portability Flags:

176.gcc: -ma -qlanglvl=ansi -DHOST_WORDS_BIG_ENDIAN
186.crafty: -DAIX
252.eon: -DNEED_EXPLICIT_SPECIALIZATION
253.perlbmk: -DSPEC_CPU2000_AIX
254.gap: -DSYS_IS_BSD -DSYS_STRING_H -DSYS_HAS_TIME_PROTO -DSYS_HAS_MALLOC_PROTO -DSYS_HAS_CALLOC_PROTO
300.twolf: -DHAVE_SIGNED_CHAR

Base Flags:

C: -O3 -Q=500 -qarch=rs64b -lhm
C++: -O3 -qarch=ppc -qtune=rs64b

Note: Feedback directed optimization was not used.
Note: 2 CPUs were installed, but only one was enabled. The service processor menu was used to disable one processor.