CINT2000 Result

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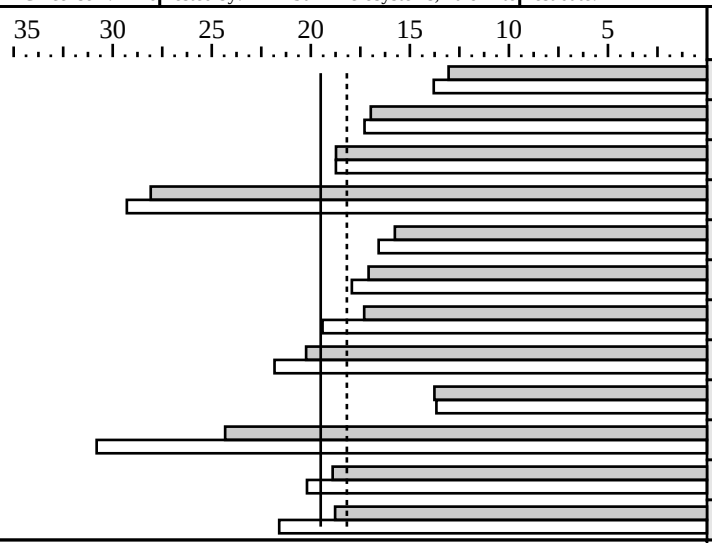
Sun Microsystems
Sun Enterprise 3500/4500

SPECint_rate2000 = 19.5

SPECint_rate_base2000 = 18.2

SPEC license #: 6 | Tested by: Sun Microsystems, Palo Alto | Test date: Sep-2000 | Hardware Avail: Dec-1998 | Software Avail: Oct-2000



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	8	996	13.0	8	942	13.8
175.vpr	8	765	17.0	8	751	17.3
176.gcc	8	545	18.7	8	545	18.7
181.mcf	8	595	28.1	8	570	29.3
186.crafty	8	589	15.8	8	560	16.6
197.parser	8	978	17.1	8	932	17.9
252.eon	8	697	17.3	8	622	19.4
253.perlbmk	8	825	20.2	8	765	21.8
254.gap	8	742	13.8	8	747	13.7
255.vortex	8	725	24.3	8	572	30.8
256.bzip2	8	737	18.9	8	689	20.2
300.twolf	8	1483	18.8	8	1289	21.6

Hardware

CPU:	UltraSPARC-II
CPU MHz:	400
FPU:	Integrated
CPU(s) enabled:	8 cores, 8 chips, 1 core/chip
CPU(s) orderable:	1-8/1-14
Parallel:	None
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	8MB(I+D) off chip
L3 Cache:	None
Other Cache:	None
Memory:	8GB
Disk Subsystem:	6 x 9GB
Other Hardware:	--

Software

Operating System:	Solaris 8
Compiler:	Forte Developer 6 update 1
File System:	UFS
System State:	multiuser

Notes/Tuning Information

```
Portability:
    176.gcc:     -Dalloca=__builtin_alloca -DHOST_WORDS_BIG_ENDIAN
    186.crafty:  -DSUN
    252.eon:     -library=iostream,no%Cstd
    253.perlbmk: -DSPEC_CPU2000_SOLARIS
    254.gap:     -DSYS_IS_USG -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
                 -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_IOCTL_PROTO

Base: -fast -xcrossfile -xprofile

Peak:
    164.gzip:    -fast -xalias_level=std -xcrossfile -xprofile
    175.vpr:     -fast -xalias_level=std -xcrossfile -xdepend -xregs=syst -xprofile
    176.gcc:     -fast -xcrossfile -Wc,-Qgsched-trace_late=1,-Qgsched-T4 -xprofile
   181.mcf:    -fast -xcrossfile -xdepend -Wc,-Qgsched-trace_late=1,-Qgsched-T4 -xsafe=mem -xprofile
    186.crafty:  -fast -xalias_level=strong -xcrossfile -xregs=syst
                 -Wc,-Qgsched-trace_late=1,-Qgsched-T4 -xprofile
    197.parser:  -fast -xalias_level=strong -xcrossfile -xdepend -xregs=syst
                 -Wc,-Qgsched-trace_late=1,-Qgsched-T4 -xprofile
```

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Notes/Tuning Information (Continued)

```
     252.eon:     -fast -xcrossfile -xsafe=mem -Qoption iropt -Mt500,-restrict_g,-restrict
                   -Qoption cg -Qgsched-trace_late=1,-Qgsched-T4 -xprofile
     253.perlbmk: -fast -xalias_level=std -xcrossfile -xsafe=mem -xprofile
                   -Wc,-Qgsched-trace_late=1,-Qgsched-T4,-Qgsched-trace_spec_load=1
     254.gap:     -fast -xarch=v8 -xalias_level=std -xcrossfile -Wc,-Qiselect-funcalign=32 -xprofile
     255.vortex:  -fast -xsafe=mem -xcrossfile -W2,-Aheap,-reroll=1,-Aunroll,-Ms1,-Mt500,-Mr6000,-crit
                   -Wc,-Qdepgraph-early_cross_call=1 -Wc,-Qiselect-funcalign=32 -Wc,-Qpeep-Sh0
                   -xrestrict -xdepend -Wc,-Qgsched-trace_late=1,-Qgsched-T4 -xprofile
     256.bzip2:    -fast -xalias_level=strong -xarch=v8 -W2,-Abopt -xcrossfile
                   -Wc,-Qiselect-funcalign=32 -xdepend -xregs=syst -xprofile
     300.twolf:    -fast -xalias_level=strong -W2,-whole -xsafe=mem -xprofile
ONESTEP=yes for both base and peak
```