CINT2000 Result

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Advanced Micro Devices

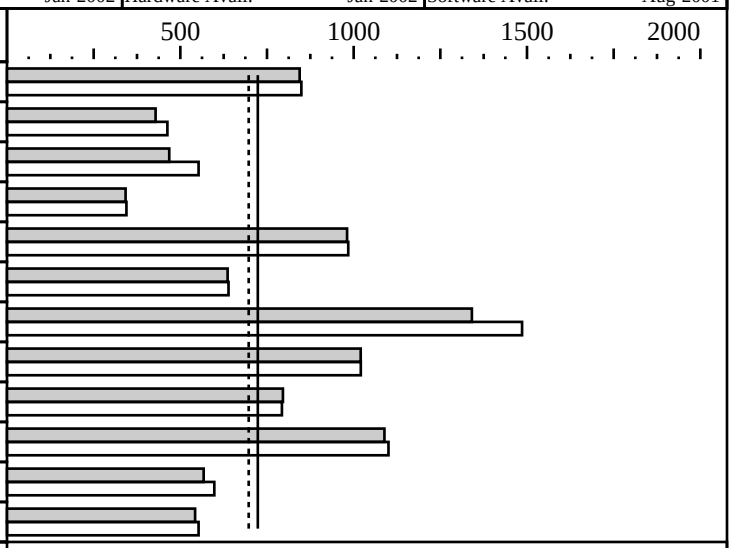
Epox 8KHA+ Motherboard, AMD Athlon (TM) XP 2000+

SPECint2000 = 724

SPECint_base2000 = 697

SPEC license #: 49 | Tested by: AMD Austin TX | Test date: Jan-2002 | Hardware Avail: Jan-2002 | Software Avail: Aug-2001

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	166	844	165	849
175.vpr	1400	327	429	303	463
176.gcc	1100	235	468	199	553
181.mcf	1800	526	342	522	345
186.crafty	1000	102	981	102	984
197.parser	1800	283	636	282	639
252.eon	1300	96.9	1341	87.5	1486
253.perlbmk	1800	176	1020	176	1021
254.gap	1100	138	796	139	793
255.vortex	1900	175	1089	173	1100
256.bzip2	1500	264	567	251	598
300.twolf	3000	553	543	543	553



Hardware

CPU:	AMD Athlon (TM) XP 2000+
CPU MHz:	1667
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1
Parallel:	No
Primary Cache:	64KBI + 64KBD on chip
Secondary Cache:	256KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x256MB PC2100 DDR SDRAM CL2
Disk Subsystem:	Seagate ST320414A
Other Hardware:	None

Software

Operating System:	Windows 2000 SP1
Compiler:	Intel C++ 5.0.1 build 010727Z Microsoft Visual Studio 6.0 SP5 (libraries) MicroQuill Smartheap Library 5.0
File System:	FAT32
System State:	Default

Notes/Tuning Information

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shlW32M.lib is the SmartHeap library V5.0 from MicroQuill www.microquill.com
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Portability:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty:   -DNT_i386
253.perlbmk:  -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap:  -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Baseline C:   -QxK -Qipo +FDO shlW32M.lib
Baseline C++: -QxK -Qipo -GX -GR
Peak tuning:
164.gzip:     -O3 -QxK -Qwp_ipo -Oi- +FDO
175.vpr:      -O3 -Qxi -QaxK -Qwp_ipo +FDO
176.gcc:      -O3 -QxK -Qwp_ipo -Oi- +FDO shlW32M.lib
181.mcf:      -O3 -QxK -Qwp_ipo +FDO shlW32M.lib
186.crafty:   -O3 -QxK -Qwp_ipo -Oa +FDO shlW32M.lib
197.parser:   -O3 -Qxi -QaxK -Qwp_ipo +FDO
252.eon:      -O3 -QxiM -Qwp_ipo +FDO
253.perlbmk:  -O3 -QxK -Qipo +FDO shlW32M.lib
254.gap:      -O3 -QxK -Qipo -Oi- +FDO shlW32M.lib
255.vortex:   -O3 -QxK -Qwp_ipo +FDO shlW32M.lib
```

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Notes/Tuning Information (Continued)

```
 256.bzip2:    -O3 -QxK -Oa -Qwp_ipo
 300.twolf:    -O3 -QxK -Qipo -Oa +FDO shlW32M.lib
 ONESTEP is used for all base and peak runs
The tested system can be assembled using an ATX case such as the Antec KS-282,
a 300W power supply such as the Sparkle FSP300-60GT, and a PCI or AGP video card.
The System bus runs at 266MHz
System BIOS Settings: Date 11/2/01, Optimized defaults. Memory settings: Turbo, 4-way interleave
```