CINT2000 Result

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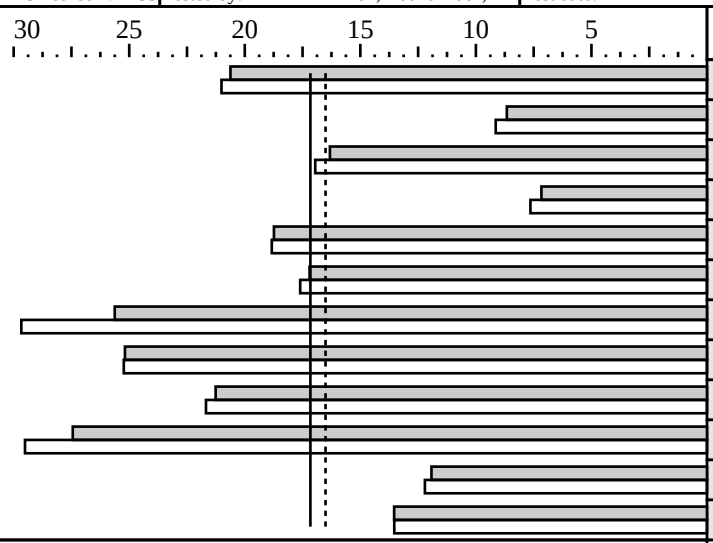
Dell PowerEdge 2650 (2.4 GHz Xeon)

SPECint_rate2000 = 17.2

SPECint_rate_base2000 = 16.5

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Mar-2002 | Hardware Avail: May-2002 | Software Avail: Jun-2001



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	158	20.6	2	155	21.0
175.vpr	2	375	8.66	2	355	9.14
176.gcc	2	156	16.3	2	151	17.0
181.mcf	2	583	7.16	2	547	7.64
186.crafty	2	124	18.7	2	123	18.8
197.parser	2	243	17.2	2	237	17.6
252.eon	2	118	25.6	2	102	29.7
253.perlbmk	2	166	25.2	2	165	25.2
254.gap	2	120	21.3	2	118	21.7
255.vortex	2	161	27.4	2	149	29.5
256.bzip2	2	292	11.9	2	285	12.2
300.twolf	2	514	13.5	2	514	13.5

Hardware

CPU:	Intel Xeon
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	2 cores, 2 chips, 1 core/chip
CPU(s) orderable:	1, 2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 8KB(D) on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4GB PC1600 (DDR 200)
Disk Subsystem:	1 x 9.2GB Quantum Atlas 10K II U160
Other Hardware:	

Software

Operating System:	Windows 2000 Professional (SP2)
Compiler:	Intel C++ Compiler 5.0.1 (010922Z) Microsoft Visual C++ 6.0 (SP5) MicroQuill SmartHeap Library 5.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
PORTABILITY FLAGS
  176.gcc:     -Dalloca=_alloca /F10000000
  186.crafty:  -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
FEEDBACK-DIRECTED OPTIMIZATION
  FDO:         PASS1= -Qprof_gen  PASS2= -Qprof_use
BASE TUNING
  C:           -Qipo +FDO -QxW shlW32M.lib
  C++:         -Qipo -QxW -GX -GR
PEAK TUNING
  164.gzip:    -Qipo +FDO -QxW -Oi- -Oa
  175.vpr:     -Qwp_ipo +FDO -QxW
  176.gcc:     -Qipo +FDO -QxW shlW32M.lib
  181.mcf:     -Qipo +FDO -QxW shlW32M.lib
  186.crafty:  -Qipo +FDO -QxW -Oa shlW32M.lib
  197.parser:  -Qipo +FDO -QxW
  252.eon:     -Qipo +FDO -QxW
  253.perlbmk: -Qipo +FDO -QxW shlW32M.lib
  254.gap:     -Qipo +FDO -QaxW
```

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Notes/Tuning Information (Continued)

```
    255.vortex:  -Qwp_ipo +FDO -QxW -Oa
    256.bzip2:   -Qipo +FDO -QxW shlW32M.lib
    300.twolf:   -Qipo +FDO -QxW shlW32M.lib
  EXTRA LIBRARIES
    shlW32M.lib: MicroQuill SmartHeap Library 5.0
                 www.microquill.com
2 CPU configuration achieved by editing boot.ini
```