CINT2000 Result

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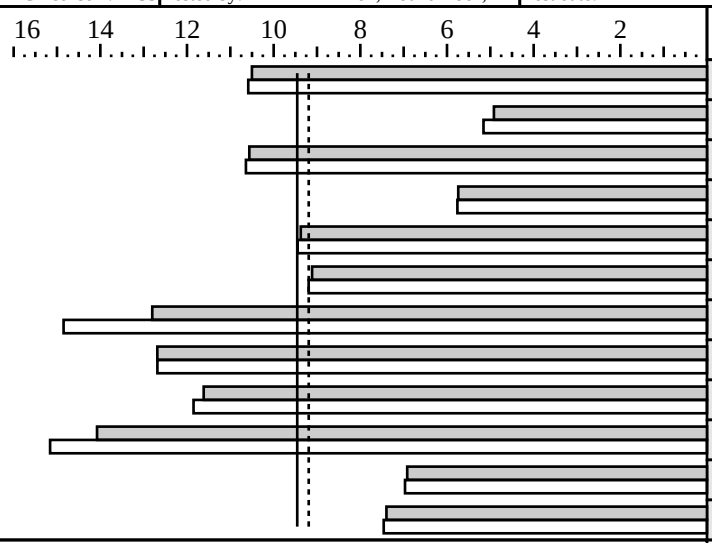
Dell
PowerEdge 2650 (2.4 GHz Xeon)

SPECint_rate2000 = 9.46

SPECint_rate_base2000 = 9.19

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Mar-2002 | Hardware Avail: May-2002 | Software Avail: Jun-2001



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	1	155	10.5	1	153	10.6
175.vpr	1	330	4.92	1	315	5.16
176.gcc	1	121	10.6	1	120	10.6
181.mcf	1	364	5.74	1	362	5.76
186.crafty	1	124	9.37	1	123	9.45
197.parser	1	229	9.11	1	227	9.19
252.eon	1	118	12.8	1	102	14.8
253.perlbmk	1	165	12.7	1	165	12.7
254.gap	1	110	11.6	1	108	11.8
255.vortex	1	157	14.1	1	145	15.2
256.bzip2	1	252	6.92	1	250	6.97
300.twolf	1	470	7.40	1	466	7.46

Hardware

CPU:	Intel Xeon
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1, 2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 8KB(D) on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4GB PC1600 (DDR 200)
Disk Subsystem:	1 x 9.2GB Quantum Atlas 10K II U160
Other Hardware:	

Software

Operating System:	Windows 2000 Professional (SP2)
Compiler:	Intel C++ Compiler 5.0.1 (010922Z) Microsoft Visual C++ 6.0 (SP5) MicroQuill SmartHeap Library 5.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

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PORTABILITY FLAGS

  176.gcc:     -Dalloca=_alloca /F10000000
  186.crafty:  -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

FEEDBACK-DIRECTED OPTIMIZATION

  FDO:         PASS1= -Qprof_gen  PASS2= -Qprof_use

BASE TUNING

  C:           -Qipo +FDO -QxW shlW32M.lib
  C++:         -Qipo -QxW -GX -GR

PEAK TUNING

  164.gzip:    -Qipo +FDO -QxW -Oi- -Oa
  175.vpr:     -Qwp_ipo +FDO -QxW
```

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Notes/Tuning Information (Continued)

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  176.gcc:     -Qipo +FDO -QxW shlW32M.lib
  181.mcf:     -Qipo +FDO -QxW shlW32M.lib
  186.crafty:  -Qipo +FDO -QxW -Oa shlW32M.lib
  197.parser:  -Qipo +FDO -QxW
  252.eon:     -Qipo +FDO -QxW
  253.perlbmk: -Qipo +FDO -QxW shlW32M.lib
  254.gap:     -Qipo +FDO -QaxW
  255.vortex:  -Qwp_ipo +FDO -QxW -Oa
  256.bzip2:   -Qipo +FDO -QxW shlW32M.lib
  300.twolf:   -Qipo +FDO -QxW shlW32M.lib

EXTRA LIBRARIES

  shlW32M.lib: MicroQuill SmartHeap Library 5.0
               www.microquill.com

1 CPU configuration achieved by editing boot.ini
```