# CINT2000 Result

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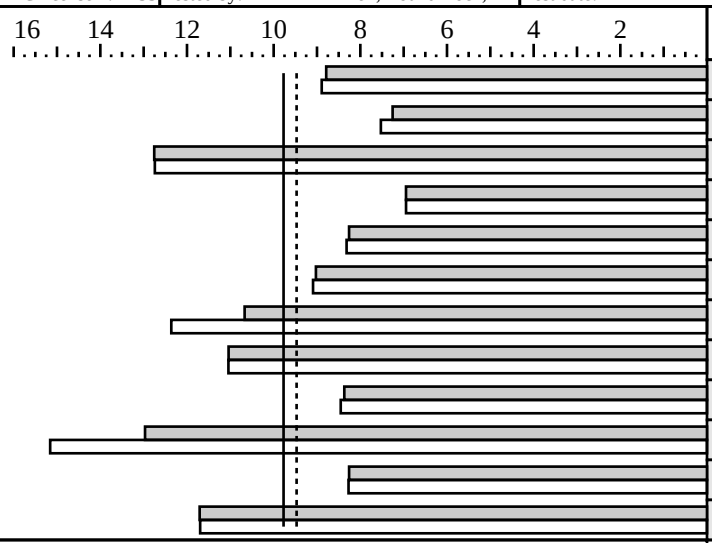
## Dell
## PowerEdge 6650 (2.0 GHz Xeon MP)

SPECint_rate2000 = 9.77

SPECint_rate_base2000 = 9.47

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Sep-2002 | Hardware Avail: Nov-2002 | Software Avail: Jun-2001




| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|---|---|---|---|---|---|---|
| 164.gzip | 1 | 185 | 8.79 | 1 | 183 | 8.89 |
| 175.vpr | 1 | 224 | 7.26 | 1 | 216 | 7.53 |
| 176.gcc | 1 | 100 | 12.8 | 1 | 100 | 12.7 |
| 181.mcf | 1 | 301 | 6.95 | 1 | 301 | 6.95 |
| 186.crafty | 1 | 140 | 8.26 | 1 | 139 | 8.32 |
| 197.parser | 1 | 231 | 9.02 | 1 | 230 | 9.09 |
| 252.eon | 1 | 141 | 10.7 | 1 | 122 | 12.4 |
| 253.perlbmk | 1 | 189 | 11.0 | 1 | 189 | 11.0 |
| 254.gap | 1 | 152 | 8.37 | 1 | 151 | 8.45 |
| 255.vortex | 1 | 170 | 13.0 | 1 | 145 | 15.2 |
| 256.bzip2 | 1 | 211 | 8.26 | 1 | 210 | 8.27 |
| 300.twolf | 1 | 297 | 11.7 | 1 | 298 | 11.7 |

### Hardware

| | |
|---|---|
| CPU: | Intel Xeon (400 MHz system bus) |
| CPU MHz: | 2000 |
| FPU: | Integrated |
| CPU(s) enabled: | 1 core, 1 chip, 1 core/chip (Hyper-Threading Technology disabled) |
| CPU(s) orderable: | 1, 2, 4 |
| Parallel: | No |
| Primary Cache: | 12K(I) micro-ops + 8KB(D) on chip |
| Secondary Cache: | 512KB(I+D) on chip |
| L3 Cache: | 2048KB(I+D) on chip |
| Other Cache: | None |
| Memory: | 4 x 128MB DDR200 |
| Disk Subsystem: | 1 x 36GB SEAGATE ST336706LC |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Windows 2000 Advanced Server (SP2) |
| Compiler: | Intel C++ Compiler 5.0.1 (020125Z)<br>Microsoft Visual C++ 6.0 (SP5)<br>MicroQuill SmartHeap Library 5.0 |
| File System: | NTFS |
| System State: | Hyper-Threading Technology disabled in BIOS |

## Notes/Tuning Information

```
PORTABILITY FLAGS
  176.gcc:     -Dalloca=_alloca /F10000000
  186.crafty:  -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:     -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
FEEDBACK-DIRECTED OPTIMIZATION
  FDO:         PASS1= -Qprof_gen  PASS2= -Qprof_use
BASE TUNING
  C:           -Qipo +FDO -QxW shlW32M.lib
  C++:         -Qipo -QxW -GX -GR
PEAK TUNING
  164.gzip:    -Qipo +FDO -QxW -Oi- -Oa
  175.vpr:     -Qwp_ipo +FDO -QxW
  176.gcc:     -Qipo +FDO -QxW shlW32M.lib
  181.mcf:     -Qipo +FDO -QxW shlW32M.lib
  186.crafty:  -Qipo +FDO -QxW -Oa shlW32M.lib
  197.parser:  -Qipo +FDO -QxW
  252.eon:     -Qipo +FDO -QxW
  253.perlbmk: -Qipo +FDO -QxW shlW32M.lib
  254.gap:     -Qipo +FDO -QaxW
```

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## Notes/Tuning Information (Continued)

```
    255.vortex:  -Qwp_ipo +FDO -QxW -Oa
    256.bzip2:   -Qipo +FDO -QxW shlW32M.lib
    300.twolf:   -Qipo +FDO -QxW shlW32M.lib
  EXTRA LIBRARIES
    shlW32M.lib: MicroQuill SmartHeap Library 5.0
                 www.microquill.com
```