CINT2000 Result

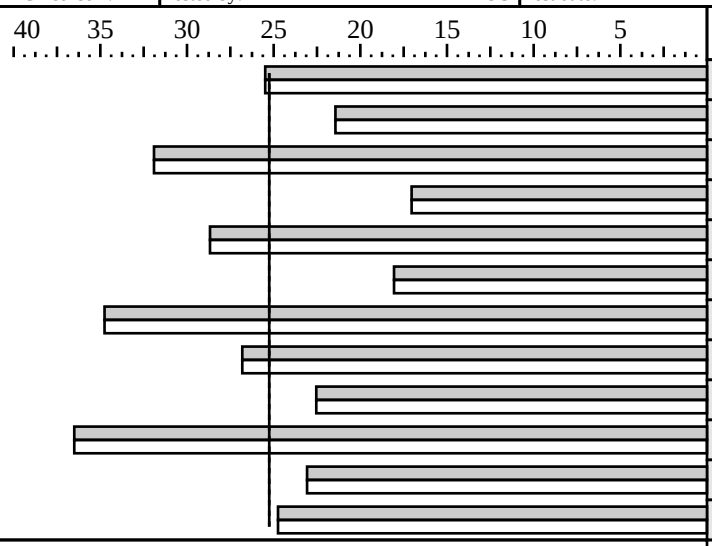
Copyright ©1999-2004, Standard Performance Evaluation Corporation

SGI
SGI Altix 3000 (900MHz, Itanium 2)

SPECint_rate2000 = 25.3

SPECint_rate_base2000 = 25.3

SPEC license #: 4 | Tested by: SGI | Test date: Mar-2003 | Hardware Avail: Feb-2003 | Software Avail: Mar-2003



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	255	25.5	4	255	25.5
175.vpr	4	303	21.4	4	303	21.4
176.gcc	4	160	31.9	4	160	31.9
181.mcf	4	490	17.0	4	490	17.0
186.crafty	4	162	28.7	4	162	28.7
197.parser	4	463	18.1	4	463	18.1
252.eon	4	174	34.8	4	174	34.8
253.perlbmk	4	312	26.8	4	312	26.8
254.gap	4	226	22.5	4	226	22.5
255.vortex	4	242	36.5	4	242	36.5
256.bzip2	4	302	23.1	4	302	23.1
300.twolf	4	562	24.7	4	562	24.7

Hardware

CPU:	Intel Itanium 2
CPU MHz:	900
FPU:	Integrated
CPU(s) enabled:	4 cores, 4 chips, 1 core/chip
CPU(s) orderable:	4-64
Parallel:	No
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	1.5MB (on chip) per CPU
Other Cache:	N/A
Memory:	8 GB (16*512MB DIMMS per 4cpu module)
Disk Subsystem:	1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
Other Hardware:	None

Software

Operating System:	SGI ProPack(TM) v2.1
Compiler:	Intel(R) C++ Compiler for Linux 7.1 (Build 20030307)
File System:	xfs
System State:	Single-user

Notes/Tuning Information

```
+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -ipo -O3 +FDO
  C++ programs:     -ipo -O2 +FDO -ansi_alias

Portability Flags:
  176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
  186.crafty: -DLINUX_i386
  252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST
  253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
  254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
           -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
  255.vortex: -DSPEC_CPU2000_LP64

Processes were bound to CPUs using dplace.

Peak flags same as baseline (basepeak=true set globally).
```