CINT2000 Result

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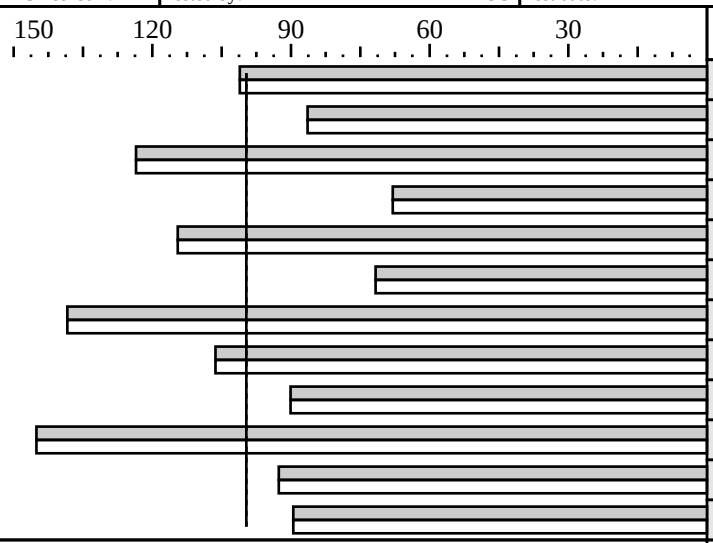
SGI
SGI Altix 3000 (900MHz, Itanium 2)

SPECint_rate2000 = 99.7

SPECint_rate_base2000 = 99.7

SPEC license #: 4 | Tested by: SGI | Test date: Mar-2003 | Hardware Avail: Feb-2003 | Software Avail: Mar-2003



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	16	257	101	16	257	101
175.vpr	16	301	86.4	16	301	86.4
176.gcc	16	165	124	16	165	124
181.mcf	16	491	68.0	16	491	68.0
186.crafty	16	162	115	16	162	115
197.parser	16	466	71.7	16	466	71.7
252.eon	16	174	138	16	174	138
253.perlbmk	16	314	106	16	314	106
254.gap	16	227	90.1	16	227	90.1
255.vortex	16	243	145	16	243	145
256.bzip2	16	301	92.6	16	301	92.6
300.twolf	16	622	89.5	16	622	89.5

Hardware

CPU:	Intel Itanium 2
CPU MHz:	900
FPU:	Integrated
CPU(s) enabled:	16 cores, 16 chips, 1 core/chip
CPU(s) orderable:	4-64
Parallel:	No
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	1.5MB (on chip) per CPU
Other Cache:	N/A
Memory:	32 GB (16*512MB DIMMS per 4cpu module)
Disk Subsystem:	1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
Other Hardware:	None

Software

Operating System:	SGI ProPack(TM) v2.1
Compiler:	Intel(R) C++ Compiler for Linux 7.1 (Build 20030307)
File System:	xfs
System State:	Single-user

Notes/Tuning Information

```
+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -ipo -O3 +FDO
  C++ programs:     -ipo -O2 +FDO -ansi_alias

Portability Flags:
  176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
  186.crafty: -DLINUX_i386
  252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST
  253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
  254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
           -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
  255.vortex: -DSPEC_CPU2000_LP64

Processes were bound to CPUs using dplace.

Peak flags same as baseline (basepeak=true set globally).
```