CINT2000 Result

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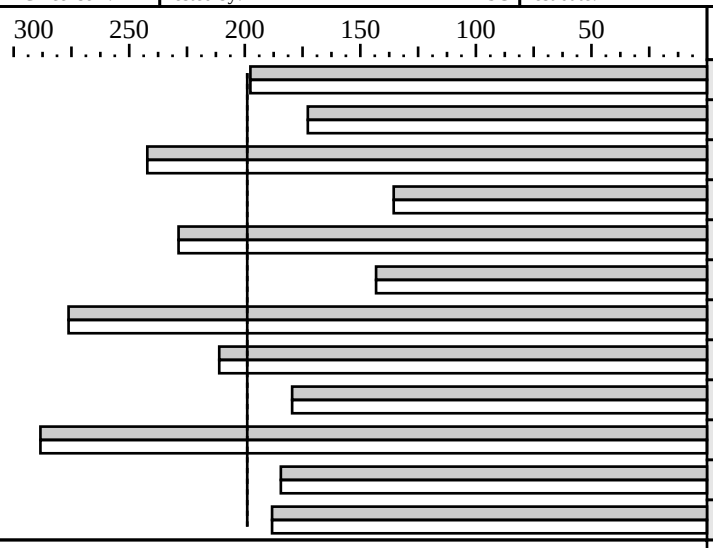
SGI
SGI Altix 3000 (900MHz, Itanium 2)

SPECint_rate2000 = 199

SPECint_rate_base2000 = 199

SPEC license #: 4 | Tested by: SGI | Test date: Mar-2003 | Hardware Avail: Feb-2003 | Software Avail: Mar-2003



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	32	263	198	32	263	198
175.vpr	32	301	173	32	301	173
176.gcc	32	169	242	32	169	242
181.mcf	32	493	136	32	493	136
186.crafty	32	162	229	32	162	229
197.parser	32	467	143	32	467	143
252.eon	32	175	276	32	175	276
253.perlbmk	32	317	211	32	317	211
254.gap	32	227	180	32	227	180
255.vortex	32	245	288	32	245	288
256.bzip2	32	302	184	32	302	184
300.twolf	32	592	188	32	592	188

Hardware

CPU:	Intel Itanium 2
CPU MHz:	900
FPU:	Integrated
CPU(s) enabled:	32 cores, 32 chips, 1 core/chip
CPU(s) orderable:	4-64
Parallel:	No
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	1.5MB (on chip) per CPU
Other Cache:	N/A
Memory:	64 GB (16*512MB DIMMS per 4cpu module)
Disk Subsystem:	1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
Other Hardware:	None

Software

Operating System:	SGI ProPack(TM) v2.1
Compiler:	Intel(R) C++ Compiler for Linux 7.1 (Build 20030307)
File System:	xfs
System State:	Single-user

Notes/Tuning Information

```
+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -ipo -O3 +FDO
  C++ programs:     -ipo -O2 +FDO -ansi_alias

Portability Flags:
  176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
  186.crafty: -DLINUX_i386
  252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST
  253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
  254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
           -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
  255.vortex: -DSPEC_CPU2000_LP64

Processes were bound to CPUs using dplace.

Peak flags same as baseline (basepeak=true set globally).
```