



CINT2000 Result

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Einix A4800

SPECint2000 = **933**
SPECint_base2000 = **880**

SPEC license #: 49 Tested by: AMD Austin TX Test date: Apr-2003 Hardware Avail: Jul-2003 Software Avail: May-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	183	764	177	789	
175.vpr	1400	182	771	180	779	
176.gcc	1100	120	918	107	1025	
181.mcf	1800	321	560	292	616	
186.crafty	1000	104	957	105	957	
197.parser	1800	216	835	211	853	
252.eon	1300	131	996	110	1181	
253.perlbmk	1800	195	923	179	1004	
254.gap	1100	113	970	113	973	
255.vortex	1900	137	1384	137	1384	
256.bzip2	1500	191	786	180	831	
300.twolf	3000	328	916	289	1037	

Hardware

CPU: AMD Opteron 140, 1.4 GHz
CPU MHz: 1400
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip
CPU(s) orderable: 1,2,4
Parallel: No
Primary Cache: 64KBI + 64KBD on chip
Secondary Cache: 1024KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 4x512MB PC2700 DDR ECC Reg SDRAM CL2.5
Disk Subsystem: IDE 7200 RPM
Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise Edition
Compiler: Intel C/C++ 7.0 build 20021212Z
Microsoft Visual Studio .NET (libraries)7.0.9466
MicroQuill SmartHeap Library 6.0
File System: NTFS
System State: Default

Notes/Tuning Information

shlw32M6.lib is the SmartHeap library V6.0 from MicroQuill www.microquill.com
+FD0: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability:

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Baseline C:

+FD0 -O3 -QxW -Qipo

Baseline C++:

+FD0 -Qipo -GX -GR

Peak tuning:

164.gzip: +FD0 -O3 -QaxK -Qipo -Oi-
175.vpr: +FD0 -O3 -QxW -Qipo
176.gcc: +FD0 -O3 -QxK -Qipo -Oi-
181.mcf: +FD0 -Qipo -Oa
186.crafty: +FD0 -O3 -QxW -Qipo
197.parser: +FD0 -O3 -QxW -Qipo -Oa
252.eon: +FD0 -O3 -QaxW -Qipo -Zp4
253.perlbmk: +FD0 -O3 -Qipo -Oa shlw32M6.lib
254.gap: +FD0 -O3 -QxW -Qipo
255.vortex: basepeak=1



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Notes/Tuning Information (Continued)

256.bzip2: +FD0 -Qipo -Oa -Qunroll1
300.twolf: +FD0 -Qxi -Qipo shlw32M6.lib
ONESTEP is used for all base and peak runs