CINT2000 Result

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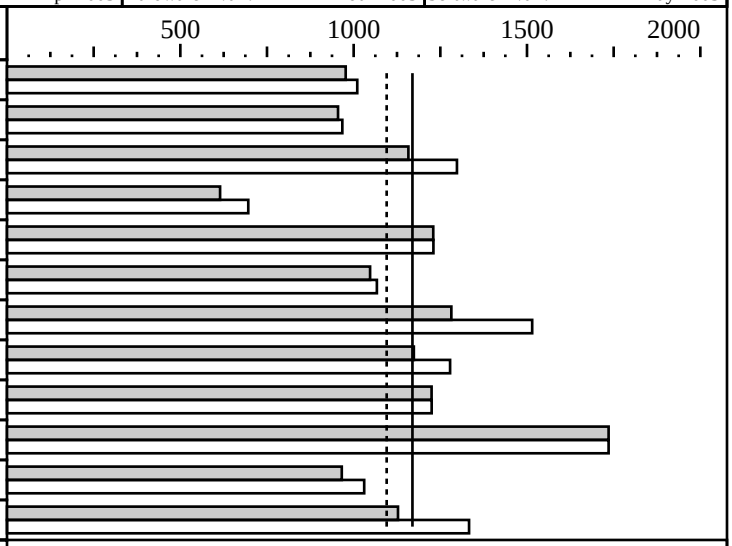
Einux
A4800

SPECint2000 = 1170

SPECint_base2000 = 1095

SPEC license #: 49 | Tested by: AMD Austin TX | Test date: Apr-2003 | Hardware Avail: Jul-2003 | Software Avail: May-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	143	977	139	1010
175.vpr	1400	147	955	145	967
176.gcc	1100	95.0	1158	84.8	1298
181.mcf	1800	293	615	259	696
186.crafty	1000	81.3	1230	81.3	1230
197.parser	1800	172	1047	169	1067
252.eon	1300	101	1282	85.8	1515
253.perlbmk	1800	153	1174	141	1278
254.gap	1100	89.8	1225	89.8	1225
255.vortex	1900	109	1735	109	1735
256.bzip2	1500	155	966	146	1030
300.twolf	3000	266	1128	225	1333



Hardware

CPU:	AMD Opteron 144, 1.8 GHz
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1,2,4
Parallel:	No
Primary Cache:	64KBI + 64KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4x512MB PC2700 DDR ECC Registered SDRAM CL2.5
Disk Subsystem:	IDE 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition
Compiler:	Intel C/C++ 7.0 build 20021212Z Microsoft Visual Studio .NET (libraries) 7.0.9466 MicroQuill Smartheap Library 6.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
shlW32M6.lib is the SmartHeap library V6.0 from MicroQuill www.microquill.com
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Portability:
  176.gcc:      -Dalloca=_alloca /F10000000
  186.crafty:   -DNT_i386
  253.perlbmk:  -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:      -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Baseline C:           +FD0 -O3 -QxW  -Qipo
Baseline C++:         +FD0           -Qipo -GX  -GR
Peak tuning:
  164.gzip:           +FD0 -O3 -QaxK -Qipo -Oi-
  175.vpr:            +FD0 -O3 -QxW  -Qipo
  176.gcc:            +FD0 -O3 -QxK  -Qipo -Oi-
  181.mcf:            +FD0           -Qipo -Oa
  186.crafty:         +FD0 -O3 -QxW  -Qipo
  197.parser:         +FD0 -O3 -QxW  -Qipo -Oa
  252.eon:            +FD0 -O3 -QaxW -Qipo -Zp4
  253.perlbmk:        +FD0 -O3       -Qipo -Oa  shlW32M6.lib
  254.gap:            +FD0 -O3 -QxW  -Qipo
  255.vortex:         basepeak=1
```

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Notes/Tuning Information (Continued)

```
  256.bzip2:          +FD0          -Qipo -Oa  -Qunroll1
  300.twolf:          +FD0     -Qxi  -Qipo      shlW32M6.lib
ONESTEP is used for all base and peak runs
```