CINT2000 Result

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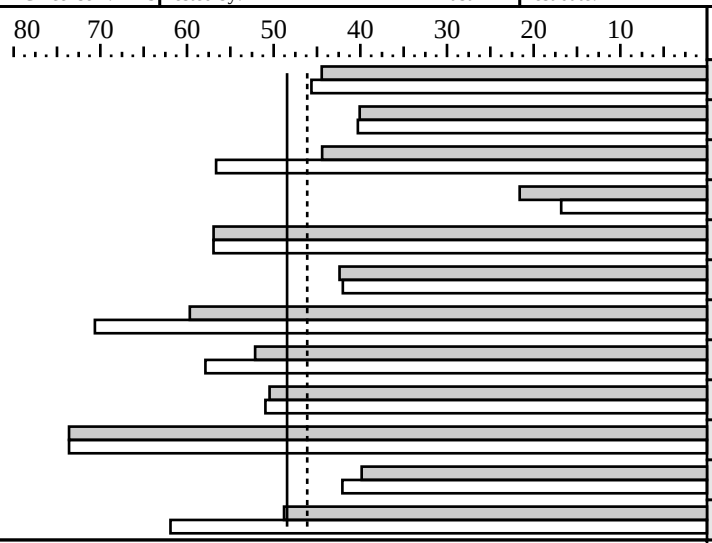
Einux
A4800

SPECint_rate2000 = 48.5

SPECint_rate_base2000 = 46.1

SPEC license #: 49 | Tested by: AMD Austin TX | Test date: Apr-2003 | Hardware Avail: Jul-2003 | Software Avail: May-2003



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	146	44.5	4	142	45.6
175.vpr	4	162	40.1	4	161	40.3
176.gcc	4	115	44.4	4	90.1	56.6
181.mcf	4	386	21.6	4	496	16.8
186.crafty	4	81.5	56.9	4	81.5	56.9
197.parser	4	197	42.4	4	199	42.0
252.eon	4	101	59.7	4	85.4	70.6
253.perlbmk	4	160	52.1	4	144	57.9
254.gap	4	101	50.5	4	100	51.0
255.vortex	4	120	73.6	4	120	73.6
256.bzip2	4	175	39.8	4	165	42.1
300.twolf	4	285	48.8	4	225	61.9

Hardware

CPU:	AMD Opteron 844, 1.8 GHz
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	4 cores, 4 chips, 1 core/chip
CPU(s) orderable:	1,2,4
Parallel:	No
Primary Cache:	64KBI + 64KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	16x512MB PC2700 DDR ECC Registered SDRAM CL2.5
Disk Subsystem:	IDE 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Server 2003 Enterprise Edition
Compiler:	Intel C/C++ 7.0 build 20021212Z Microsoft Visual Studio .NET (libraries)7.0.9466 MicroQuill Smartheap Library 6.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
shlW32M6.lib is the SmartHeap library V6.0 from MicroQuill www.microquill.com
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Portability:
  176.gcc:      -Dalloca=_alloca /F10000000
  186.crafty:   -DNT_i386
  253.perlbmk:  -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:      -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Baseline C:           +FD0 -O3 -QxW  -Qipo
Baseline C++:         +FD0           -Qipo -GX  -GR
Peak tuning:
  164.gzip:           +FD0 -O3 -QaxK -Qipo -Oi-
  175.vpr:            +FD0 -O3 -QxW  -Qipo
  176.gcc:            +FD0 -O3 -QxK  -Qipo -Oi-
  181.mcf:            +FD0           -Qipo -Oa
  186.crafty:         +FD0 -O3 -QxW  -Qipo
  197.parser:         +FD0 -O3 -QxW  -Qipo -Oa
  252.eon:            +FD0 -O3 -QaxW -Qipo -Zp4
  253.perlbmk:        +FD0 -O3       -Qipo -Oa  shlW32M6.lib
  254.gap:            +FD0 -O3 -QxW  -Qipo
  255.vortex:         basepeak=1
```

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Notes/Tuning Information (Continued)

```
  256.bzip2:          +FD0          -Qipo -Oa  -Qunroll1
  300.twolf:          +FD0     -Qxi  -Qipo      shlW32M6.lib
ONESTEP is used for all base and peak runs
```