# CINT2000 Result

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## Advanced Micro Devices

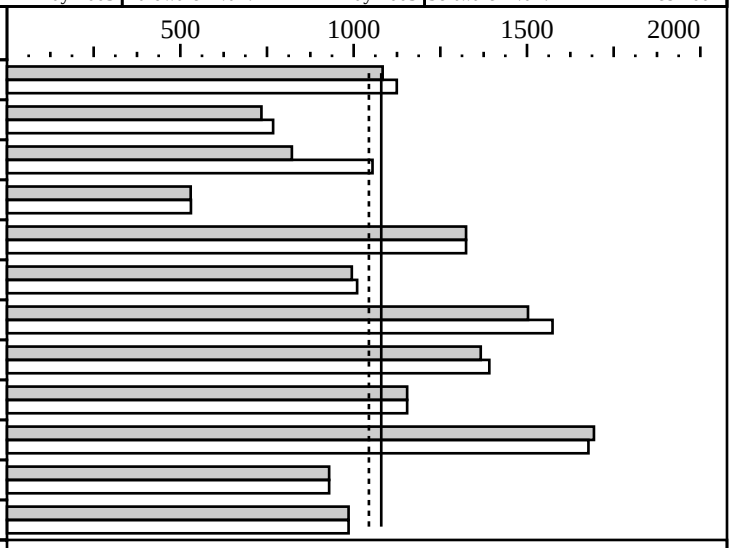
ASUS A7N8X Motherboard rev. 2.0, AMD Athlon (TM) XP 3200+

SPECint2000 = 1080

SPECint_base2000 = 1044

SPEC license #: 49 | Tested by: AMD Austin TX | Test date: May-2003 | Hardware Avail: May-2003 | Software Avail: Feb-2002

| Benchmark | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio |
|---|---|---|---|---|---|
| 164.gzip | 1400 | 129 | 1084 | 125 | 1124 |
| 175.vpr | 1400 | 191 | 734 | 182 | 768 |
| 176.gcc | 1100 | 134 | 822 | 104 | 1054 |
| 181.mcf | 1800 | 340 | 530 | 339 | 531 |
| 186.crafty | 1000 | 75.5 | 1324 | 75.5 | 1324 |
| 197.parser | 1800 | 181 | 995 | 178 | 1010 |
| 252.eon | 1300 | 86.5 | 1503 | 82.6 | 1574 |
| 253.perlbmk | 1800 | 132 | 1367 | 129 | 1391 |
| 254.gap | 1100 | 95.3 | 1154 | 95.3 | 1154 |
| 255.vortex | 1900 | 112 | 1693 | 113 | 1677 |
| 256.bzip2 | 1500 | 161 | 929 | 161 | 929 |
| 300.twolf | 3000 | 304 | 985 | 304 | 985 |




### Hardware

| | |
|---|---|
| CPU: | AMD Athlon (TM) XP AXDA3200KV4E |
| CPU MHz: | 2200 |
| FPU: | Integrated |
| CPU(s) enabled: | 1 core, 1 chip, 1 core/chip |
| CPU(s) orderable: | 1 |
| Parallel: | No |
| Primary Cache: | 64KBI + 64KBD on chip |
| Secondary Cache: | 512KB(I+D) on chip |
| L3 Cache: | N/A |
| Other Cache: | N/A |
| Memory: | 2x256MB PC3200 DDR SDRAM CL2.0 |
| Disk Subsystem: | WD1200JB-00DUA1 120GB IDE |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Windows XP Professional SP1 |
| Compiler: | Intel C++ 7.0 build 20021021Z<br>Microsoft Visual Studio .NET 7.0.9466 (libraries)<br>MicroQuill Smartheap Library 6.0 |
| File System: | NTFS |
| System State: | Default |

## Notes/Tuning Information

```
shlW32M.lib is the SmartHeap library V6.0 from MicroQuill www.microquill.com
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Portability:
176.gcc:    -Dalloca=_alloca /F10000000
186.crafty:   -DNT_i386
253.perlbmk:  -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap:    -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Baseline C:   -QxK -Qipo +FDO shlW32M.lib
Baseline C++: -QxK -Qipo -GX -GR
Peak tuning:
164.gzip:     -O3 -QxK -Qipo -Oi- -Oa +FDO shlW32M.lib
175.vpr:      -O2 -QxK -Qipo +FDO
176.gcc:      -O3 -QxK -Oi- -Qipo +FDO shlW32M.lib
181.mcf:      -O3 -QxK -Qipo +FDO shlW32M.lib
186.crafty:   basepeak=true
197.parser:   -O3 -QxK -Qipo -Oa +FDO shlW32M.lib
252.eon:      -O2 -QxK -Qipo +FDO
253.perlbmk:  -O3 -QxK -Qipo +FDO shlW32M.lib
254.gap:      basepeak=true
255.vortex:   -O3 -QxK -Qipo -Oi- +FDO shlW32M.lib
```

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## Notes/Tuning Information (Continued)

```
 256.bzip2:    basepeak=true
 300.twolf:    basepeak=true
 ONESTEP is used for all base and peak runs
The tested system can be assembled using an ATX case such as the Antec KS-282,
a 480W power supply such as the Antec True480, and a PCI or AGP video card.
The System bus runs at 400MHz
The Memory bus runs at 400MHz
Memory used in dual channel configuration
```