CINT2000 Result

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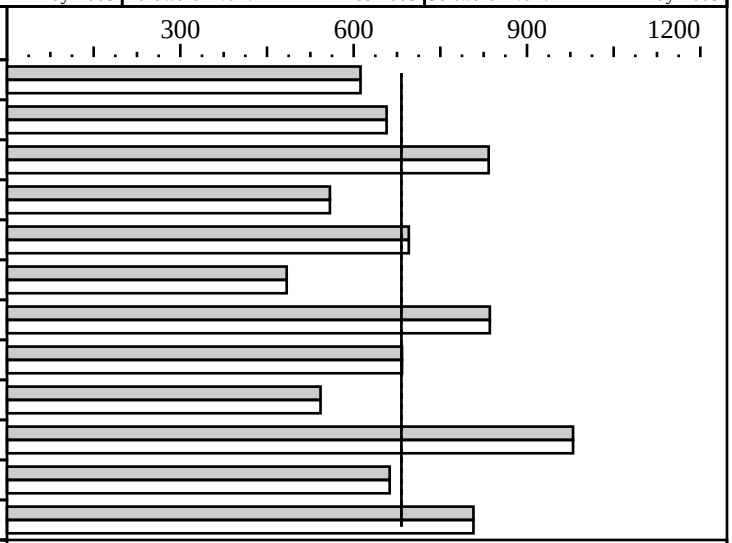
SGI
SGI Altix 3000 (1000MHz, Itanium 2)

SPECint2000 = 683

SPECint_base2000 = 683

SPEC license #: 4 | Tested by: SGI | Test date: May-2003 | Hardware Avail: Feb-2003 | Software Avail: May-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	229	612	229	612
175.vpr	1400	213	657	213	657
176.gcc	1100	132	834	132	834
181.mcf	1800	322	559	322	559
186.crafty	1000	144	695	144	695
197.parser	1800	372	484	372	484
252.eon	1300	156	836	156	836
253.perlbmk	1800	263	684	263	684
254.gap	1100	203	543	203	543
255.vortex	1900	194	980	194	980
256.bzip2	1500	226	662	226	662
300.twolf	3000	372	807	372	807



Hardware

CPU:	Intel Itanium 2
CPU MHz:	1000
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	4-64
Parallel:	No
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	3.0MB (on chip) per CPU
Other Cache:	N/A
Memory:	2 GB
Disk Subsystem:	1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
Other Hardware:	None

Software

Operating System:	SGI ProPack(TM) v2.1
Compiler:	Intel(R) C++ Compiler for Linux 7.1 (Build 20030422)
File System:	xfs
System State:	Single-user

Notes/Tuning Information

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+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -ipo -O3 +FDO
  C++ programs:     -ipo -O2 -ansi_alias +FDO

Portability Flags:
  176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca -D_LIBC
  186.crafty: -DLINUX_i386
  252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST
  253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
  254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
           -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
  255.vortex: -DSPEC_CPU2000_LP64

Processes were bound to CPUs using dplace.

Peak flags same as baseline (basepeak=true set globally).
```