CINT2000 Result

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Advanced Micro Devices

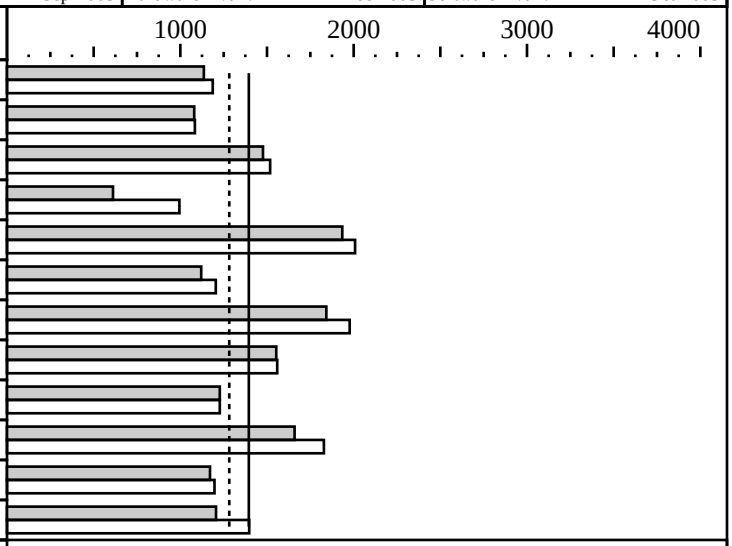
ASUS SK8N Motherboard, AMD Athlon (TM) FX-51

SPECint2000 = 1395

SPECint_base2000 = 1282

SPEC license #: 49 | Tested by: AMD, Austin, TX | Test date: Sep-2003 | Hardware Avail: Dec-2003 | Software Avail: Oct-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	123	1136	118	1187
175.vpr	1400	130	1080	129	1084
176.gcc	1100	74.5	1477	72.5	1518
181.mcf	1800	294	612	181	995
186.crafty	1000	51.7	1934	49.8	2008
197.parser	1800	161	1120	149	1205
252.eon	1300	70.6	1842	65.8	1977
253.perlbmk	1800	116	1553	115	1559
254.gap	1100	89.6	1228	89.6	1228
255.vortex	1900	115	1659	104	1828
256.bzip2	1500	128	1171	125	1197
300.twolf	3000	249	1206	215	1397



Hardware

CPU:	AMD Athlon (TM) FX-51 ADAFX51CEP5AK
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1
Parallel:	No
Primary Cache:	64KBI + 64KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x512MB PC3200 DDR SDRAM CL2.5, ECC Registered
Disk Subsystem:	Western Digital WD1200 IDE 120 GB
Other Hardware:	None

Software

Operating System:	SuSE Linux 9.0 Professional for AMD 64
Compiler:	SuSE optional gcc 3.3 compiler (20030312) (from SLES8 SP2)
File System:	Linux/reiserfs
System State:	Multi-user SuSE Run level 3

Notes/Tuning Information

```
Binaries Cross-Compiled on a system running
  SLES 8 for AMD64 (SuSE Linux enterprise server) SP2
+FDO: PASS1=-fprofile-arcs PASS2=-fbranch-probabilities
  Portability:
                PORTABILITY=-DSPEC_CPU2000_LP64 is applied to all benchmarks.
                Has no effect on 181.mcf, 197.parser, 300.twolf
   186.crafty:  CPORTABILITY=-DLINUX_i386
   176.gcc:     Approved src.alt is used
   252.eon:     CXXPORTABILITY=-DHAS_ERRLIST -DFMAX_IS_DOUBLE
   253.perlbmk: CPORTABILITY=-DSPEC_CPU2000_LINUX_I386 -DSPEC_CPU2000_NEED_BOOL
                           -DSPEC_CPU2000_GLIBC22
   254.gap:     CPORTABILITY=-DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO
                          -DSYS_HAS_SIGNAL_PROTO -DSYS_HAS_ANSI -DSYS_HAS_CALLOC_PROTO
  Baseline C:      gcc -O3 -funroll-all-loops +FDO
  Baseline C++:    g++ -O3 -funroll-all-loops +FDO
  Peak tuning:
   gzip:    -O3 -funroll-all-loops -finline-limit=900 -freduce-all-givs +FDO
   vpr:     -O3 -funroll-loops -finline-limit=1000 +FDO
   gcc:     -O3 -funroll-all-loops -finline-limit=900 +FDO
   mcf:     -O3 -funroll-all-loops -m32 +FDO
```

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Notes/Tuning Information (Continued)

```
  crafty:  -O3 -funroll-loops -fprefetch-loop-arrays
  parser:  -O3 -funroll-all-loops -m32 +FDO
  eon:     -O3 -funroll-all-loops -ffast-math -finline-limit=3000 +FDO
  perlbmk: -O3 -funroll-all-loops -finline-limit=1800 +FDO
  gap:     basepeak=true
  vortex:  -O3 -funroll-all-loops -finline-limit=1000 +FDO
  bzip2:   -O3 -funroll-all-loops -freduce-all-givs -finline-limit=2700 +FDO
  twolf:   -O3 -funroll-all-loops -finline-limit=2000 -m32 +FDO
 ONESTEP is used for all base and peak runs
 Binaries are compiled in SLES8 for AMD64
The tested system can be assembled using an ATX case such as the Antec KS-282
and a 460W power supply such as the Antec True 460
ECC disabled
```