CINT2000 Result

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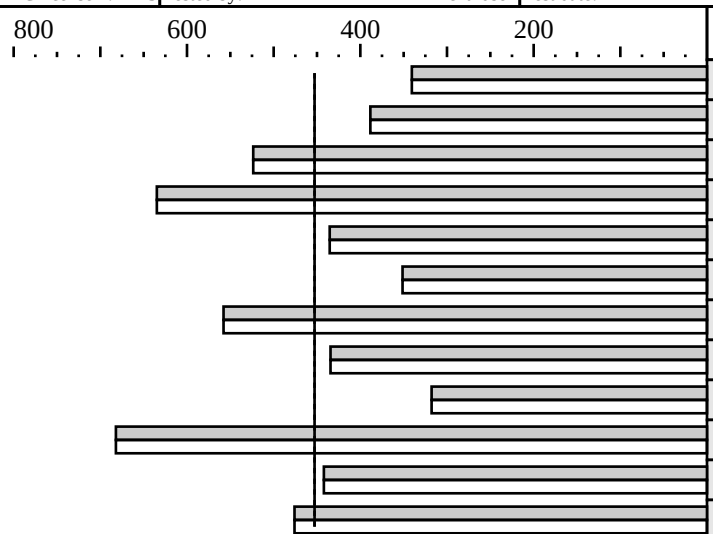
Hewlett-Packard Company

HP Integrity Superdome 32-way (1500 MHz Itanium 2)

SPECint_rate2000 = 453

SPECint_rate_base2000 = 453

SPEC license #: 3 | Tested by: HP Richardson | Test date: Sep-2003 | Hardware Avail: Oct-2003 | Software Avail: Sep-2003



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	32	153	341	32	153	341
175.vpr	32	134	388	32	134	388
176.gcc	32	78.0	524	32	78.0	524
181.mcf	32	105	635	32	105	635
186.crafty	32	85.3	435	32	85.3	435
197.parser	32	190	351	32	190	351
252.eon	32	86.5	558	32	86.5	558
253.perlbmk	32	154	434	32	154	434
254.gap	32	128	318	32	128	318
255.vortex	32	103	682	32	103	682
256.bzip2	32	126	442	32	126	442
300.twolf	32	234	476	32	234	476

Hardware

CPU:	Intel Itanium 2
CPU MHz:	1500
FPU:	Integrated
CPU(s) enabled:	32 cores, 32 chips, 1 core/chip
CPU(s) orderable:	6 to 32 by 2
Parallel:	no
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	6.0MB (on chip) per CPU
Other Cache:	N/A
Memory:	128GB (256 * 512MB DIMMs)
Disk Subsystem:	10x36GB 10K RPM SCSI disk (striped)
Other Hardware:	N/A

Software

Operating System:	HPUX11i-TCOE B.11.23
Compiler:	HP C/ANSI C Compiler B.11.23
	HP aC++ Compiler B.11.23
File System:	hfs
System State:	Multi-user

Notes/Tuning Information

```
Portability Flags
176.gcc     : -DHOST_WORDS_BIG_ENDIAN
186.crafty  : -DHP
252.eon     : -DFMAX_IS_DOUBLE
253.perlbmk : -DSPEC_CPU2000_HP
254.gap     : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
              -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO

Base Flags
  all       : +Oprofile=collect:all/+Oprofile=use
  C         : +Ofaster -exec +Odatalayout
  C++       : +Ofaster +inline_level 2 -minshared

Peak Flags
  same as baseline (basepeak=true set globally)

Kernel Tunables:
  dbc_max_pct=20
  dbc_min_pct=20
  maxdsiz=3221225472
```

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Notes/Tuning Information (Continued)

```
    maxssiz=401604608
    maxdsiz_64bit=4396972769279
    maxssiz_64bit=1073741824
    vps_pagesize=4096
    vps_ceiling=16384

  Notes:

    System was configured with 1/2 of memory interleaved and
      1/2 of memory local to each cell

    System configured as a single partition with 8 cells and
      4 processors per cell

    Processes were assigned to localities using the
      HP-UX mpsched utility

    Filesystem used for spec runs mounted fs_async
```