CINT2000 Result

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Hewlett-Packard Company

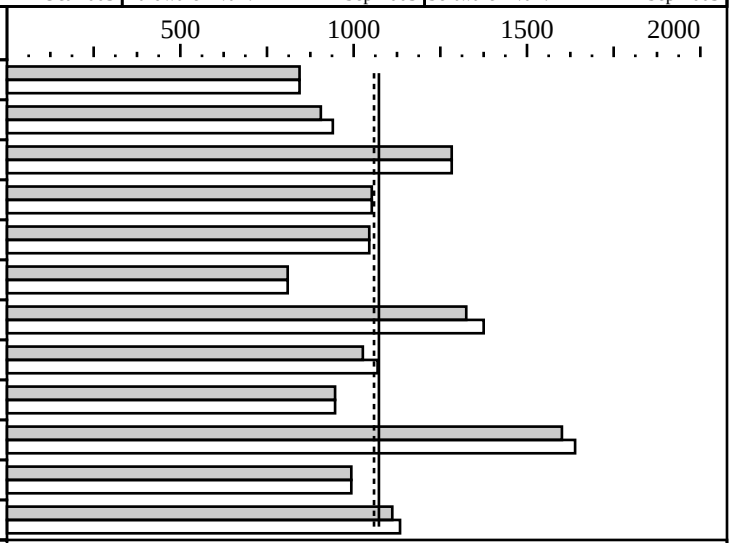
HP Integrity Server rx2600 (1300 MHz, Itanium 2)

SPECint2000 = 1073

SPECint_base2000 = 1059

SPEC license #: 3 | Tested by: HP Richardson | Test date: Oct-2003 | Hardware Avail: Sep-2003 | Software Avail: Sep-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	166	844	166	844
175.vpr	1400	155	905	149	940
176.gcc	1100	85.7	1283	85.7	1283
181.mcf	1800	171	1052	171	1052
186.crafty	1000	95.7	1045	95.7	1045
197.parser	1800	222	810	222	810
252.eon	1300	98.1	1325	94.5	1375
253.perlbmk	1800	175	1026	168	1069
254.gap	1100	116	946	116	946
255.vortex	1900	119	1601	116	1639
256.bzip2	1500	151	993	151	993
300.twolf	3000	270	1111	265	1134



Hardware

CPU:	Intel Itanium 2
CPU MHz:	1300
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1-2
Parallel:	no
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	3.0MB (on chip) per CPU
Other Cache:	N/A
Memory:	12GB (12 x 1024MB DIMMs)
Disk Subsystem:	1x36GB 15k RPM SCSI disk
Other Hardware:	N/A

Software

Operating System:	HPUX11i-TCOE B.11.23
Compiler:	HP C/ANSI C Compiler B.11.23 HP aC++ Compiler B.11.23
	PHSS_29655 aC++ Compiler PHSS_29656 HP C Compiler PHSS_29657 u2comp/be/plugin library Patch PHSS_29658 Aries
File System:	vxfs
System State:	Multi-user

Notes/Tuning Information

```
  Portability Flags
  176.gcc    : -DHOST_WORDS_BIG_ENDIAN
  186.crafty : -DHP
  252.eon    : -DFMAX_IS_DOUBLE
  253.perlbmk: -DSPEC_CPU2000_HP
  254.gap    : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
               -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO

 Base Flags
  PBO        : +Oprofile=collect:all/+Oprofile=use
  C          : PBO +Ofaster -exec +Odatalayout
  C++        : PBO +Ofaster +inline_level 2 -minshared

 Peak Flags
  164.gzip   : basepeak=true
  175.vpr    : PBO +Ofaster +Odatalayout +Onorecovery
                 +Onoptrs_to_globals -exec
               LIBS = -Wl,-aarchive_shared -Bprotected
  176.gcc    : basepeak=true
  181.mfc    : basepeak=true
```

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Notes/Tuning Information (Continued)

```
186.crafty : basepeak=true
197.parser : basepeak=true
252.eon    : PBO +Ofaster +Odatalayout +Otype_safety=ansi
               +inline_level 2 -minshared
253.perlbmk: PBO +Ofaster +Odatalayout +Onoptrs_to_globals -exec
             LIBS = -Wl,-aarchive_shared -Bprotected_def fastmem.o
254.gap    : basepeak=true
255.vortex : PBO +Ofaster +Odatalayout +Onorecovery
               +Onoptrs_to_globals -exec
             LIBS = -Wl,-aarchive_shared -Bprotected
256.bzip2  : basepeak=true
300.twolf  : PBO +Ofaster +Odatalayout +Onoptrs_to_globals -exec
             LIBS = -Wl,-aarchive_shared -Bprotected_data effmem.o

Kernel Tunables:
  maxdsiz            0x7b03a000
  maxdsiz_64bit   0x04000000000
  maxssiz            0x10000000
  maxssiz_64bit      0x40000000
  vps_ceiling             16384
  vps_pagesize             4096
  swapmem_on                  0
  dbc_max_pct                20
  dbc_min_pct                20

Note:
  EFI shell utility 'cpuconfig' used
  to deconfigure processors if necessary
```