CINT2000 Result

Copyright ©1999-2004, Standard Performance Evaluation Corporation

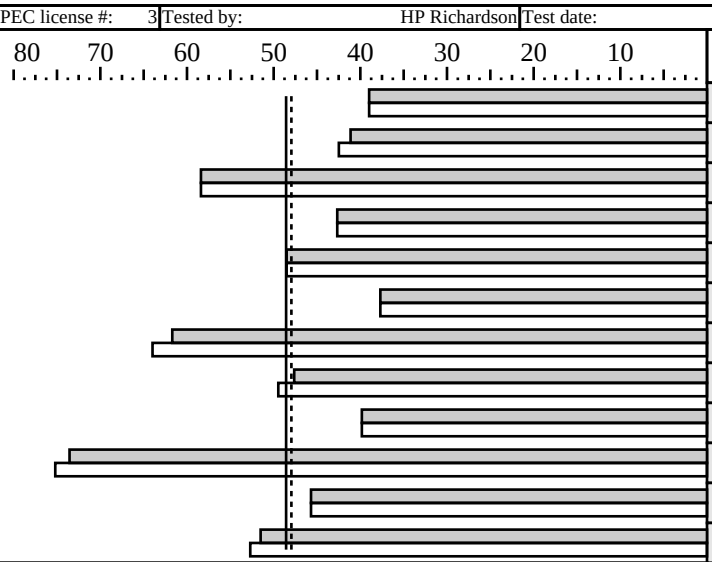
Hewlett-Packard Company

HP Integrity Server rx5670 (1300 MHz, Itanium 2)

SPECint_rate2000 = 48.6

SPECint_rate_base2000 = 48.0

SPEC license #: 3 | Tested by: HP Richardson | Test date: Oct-2003 | Hardware Avail: Sep-2003 | Software Avail: Sep-2003



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	167	39.0	4	167	39.0
175.vpr	4	158	41.1	4	153	42.5
176.gcc	4	87.4	58.4	4	87.4	58.4
181.mcf	4	196	42.7	4	196	42.7
186.crafty	4	95.7	48.5	4	95.7	48.5
197.parser	4	222	37.7	4	222	37.7
252.eon	4	97.8	61.7	4	94.3	64.0
253.perlbmk	4	175	47.6	4	169	49.5
254.gap	4	128	39.8	4	128	39.8
255.vortex	4	120	73.6	4	117	75.2
256.bzip2	4	152	45.7	4	152	45.7
300.twolf	4	270	51.5	4	264	52.7

Hardware

CPU:	Intel Itanium 2
CPU MHz:	1300
FPU:	Integrated
CPU(s) enabled:	4 cores, 4 chips, 1 core/chip
CPU(s) orderable:	1-4
Parallel:	no
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	3.0MB (on chip) per CPU
Other Cache:	N/A
Memory:	24GB (12 x 512MB DIMMs on 2 carriers)
Disk Subsystem:	1x36GB 15k RPM SCSI disk
Other Hardware:	N/A

Software

Operating System:	HPUX11i-TCOE B.11.23
Compiler:	HP C/ANSI C Compiler B.11.23 HP aC++ Compiler B.11.23 PHSS_29655 aC++ Compiler PHSS_29656 HP C Compiler PHSS_29657 u2comp/be/plugin library Patch PHSS_29658 Aries
File System:	vxfs
System State:	Multi-user

Notes/Tuning Information

```
 Portability Flags
 176.gcc    : -DHOST_WORDS_BIG_ENDIAN
 186.crafty : -DHP
 252.eon    : -DFMAX_IS_DOUBLE
 253.perlbmk: -DSPEC_CPU2000_HP
 254.gap    : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
              -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO

Base Flags
 PBO        : +Oprofile=collect:all/+Oprofile=use
 C          : PBO +Ofaster -exec +Odatalayout
 C++        : PBO +Ofaster +inline_level 2 -minshared

Peak Flags
 164.gzip   : basepeak=true
 175.vpr    : PBO +Ofaster +Odatalayout +Onorecovery
                +Onoptrs_to_globals -exec
              LIBS = -Wl,-aarchive_shared -Bprotected
 176.gcc    : basepeak=true
 181.mfc    : basepeak=true
```

Copyright ©1999-2004, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
186.crafty : basepeak=true
197.parser : basepeak=true
252.eon    : PBO +Ofaster +Odatalayout +Otype_safety=ansi
               +inline_level 2 -minshared
253.perlbmk: PBO +Ofaster +Odatalayout +Onoptrs_to_globals -exec
             LIBS = -Wl,-aarchive_shared -Bprotected_def fastmem.o
254.gap    : basepeak=true
255.vortex : PBO +Ofaster +Odatalayout +Onorecovery
               +Onoptrs_to_globals -exec
             LIBS = -Wl,-aarchive_shared -Bprotected
256.bzip2  : basepeak=true
300.twolf  : PBO +Ofaster +Odatalayout +Onoptrs_to_globals -exec
             LIBS = -Wl,-aarchive_shared -Bprotected_data effmem.o

Kernel Tunables:
  maxdsiz            0x7b03a000
  maxdsiz_64bit   0x04000000000
  maxssiz            0x10000000
  maxssiz_64bit      0x40000000
  vps_ceiling             16384
  vps_pagesize             4096
  swapmem_on                  0
  dbc_max_pct                20
  dbc_min_pct                20

Note:
  EFI shell utility 'cpuconfig' used
  to deconfigure processors if necessary
```