CINT2000 Result

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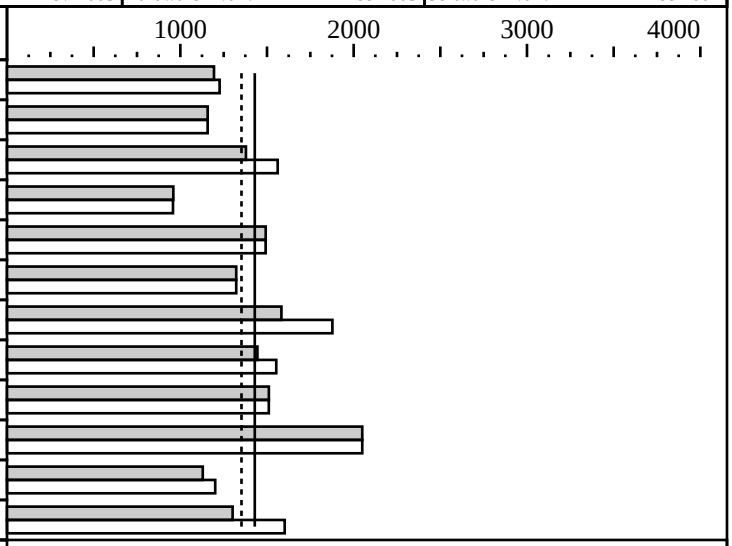
Fujitsu Siemens Computers CELSIUS V810

SPECint2000 = 1429

SPECint_base2000 = 1353

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Nov-2003 | Hardware Avail: Dec-2003 | Software Avail: Dec-2002

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	117	1194	114	1227
175.vpr	1400	121	1158	121	1158
176.gcc	1100	79.8	1378	70.4	1562
181.mcf	1800	188	960	188	958
186.crafty	1000	67.0	1493	67.0	1492
197.parser	1800	136	1322	136	1322
252.eon	1300	82.1	1583	69.3	1877
253.perlbmk	1800	125	1445	116	1553
254.gap	1100	72.8	1511	72.8	1510
255.vortex	1900	92.7	2050	92.7	2050
256.bzip2	1500	133	1129	125	1201
300.twolf	3000	230	1302	187	1602



Hardware

CPU:	AMD Opteron (TM) 248 (2.2 GHz)
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	64KBI + 64KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4x1GB PC2700 DDR SDRAM CL2.5, ECC Registered
Disk Subsystem:	ATA-100
Other Hardware:	None

Software

Operating System:	Windows XP Pro SP1
Compiler:	Intel C/C++ 7.0 build 20021212Z Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 6.0
File System:	FAT32
System State:	Default

Notes/Tuning Information

```
shlW32M6.lib is the SmartHeap library V6.0 from MicroQuill www.microquill.com
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Portability:
  176.gcc:      -Dalloca=_alloca /F10000000
  186.crafty:   -DNT_i386
  253.perlbmk:  -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap:      -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Baseline C:           +FDO -O3 -QxW  -Qipo
Baseline C++:         +FDO           -Qipo -GX  -GR
Peak tuning:
  164.gzip:           +FDO -O3 -QaxK -Qipo -Oi-
  175.vpr:            +FDO -O3 -QxW  -Qipo
  176.gcc:            +FDO -O3 -QxK  -Qipo -Oi-
  181.mcf:            +FDO           -Qipo -Oa
  186.crafty:         +FDO -O3 -QxW  -Qipo
  197.parser:         +FDO -O3 -QxW  -Qipo -Oa
  252.eon:            +FDO -O3 -QaxW -Qipo -Zp4
  253.perlbmk:        +FDO -O3       -Qipo -Oa  shlW32M6.lib
  254.gap:            +FDO -O3 -QxW  -Qipo
  255.vortex:         basepeak=1
```

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Notes/Tuning Information (Continued)

```
  256.bzip2:           +FDO           -Qipo -Oa  -Qunroll1
  300.twolf:           +FDO     -Qxi  -Qipo      shlW32M6.lib
ONESTEP is used for all base and peak runs

System tested with only one CPU present.
BIOS default settings have been used. These include:
ECC memory checking disabled,
node interleave disabled, SRAT enabled.

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http://www.fujitsu-siemens.com/countries
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