CFP2000 Result

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SGI

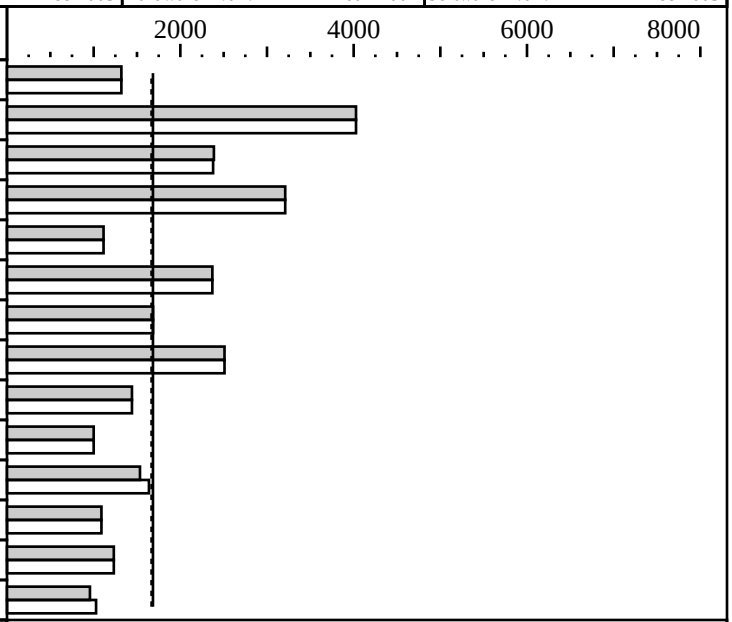
SGI Altix 350 (1400MHz/1.5MB, Itanium 2)

SPECfp2000 = 1684

SPECfp_base2000 = 1668

SPEC license #: 4 | Tested by: SGI | Test date: Dec-2003 | Hardware Avail: Jan-2004 | Software Avail: Dec-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	121	1320	121	1320
171.swim	3100	77.0	4027	77.0	4027
172.mgrid	1800	75.4	2387	75.7	2378
173.applu	2100	65.4	3210	65.4	3210
177.mesa	1400	126	1114	126	1114
178.galgel	2900	122	2370	122	2370
179.art	2600	154	1685	154	1685
183.equake	1300	51.8	2510	51.8	2510
187.facerec	1900	132	1442	132	1442
188.ammp	2200	220	1000	220	1000
189.lucas	2000	130	1534	122	1638
191.fma3d	2100	193	1090	193	1090
200.sixtrack	1100	89.3	1232	89.3	1232
301.apsi	2600	272	957	253	1028



Hardware

CPU:	Intel Itanium 2
CPU MHz:	1400
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	2-16
Parallel:	No
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	1.5MB (on chip) per CPU
Other Cache:	N/A
Memory:	4 GB (8*512MB PC2700 DIMMS)
Disk Subsystem:	1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
Other Hardware:	None

Software

Operating System:	SGI ProPack(TM) v2.3
Compiler:	Intel(R) Fortran Compiler for Linux 8.0 (Build 20031017) Intel(R) C++ Compiler for Linux 8.0 (Build 20031017)
File System:	xfs
System State:	Single-user

Notes/Tuning Information

```
+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -ipo -O3 +FDO -ansi_alias
  Fortran programs: -ipo -O3 +FDO

Portability Flags:
  178.galgel:  -FI

Peak optimization flags:
  168.wupwise: basepeak=yes
  171.swim: basepeak=yes
  172.mgrid: -O3 -ipo
  173.applu: basepeak=yes
  177.mesa: basepeak=yes
  178.galgel: basepeak=yes
  179.art: basepeak=yes
```

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Notes/Tuning Information (Continued)

```
  183.equake: basepeak=yes
  187.facerec: basepeak=yes
  188.ammp: basepeak=yes
  189.lucas: -O3 -ipo
  191.fma3d: basepeak=yes
  200.sixtrack: basepeak=yes
  301.apsi: -O3 -ipo

Processes were bound to CPUs using dplace.
Extra CPUs were disabled at firmware.
```