CFP2000 Result

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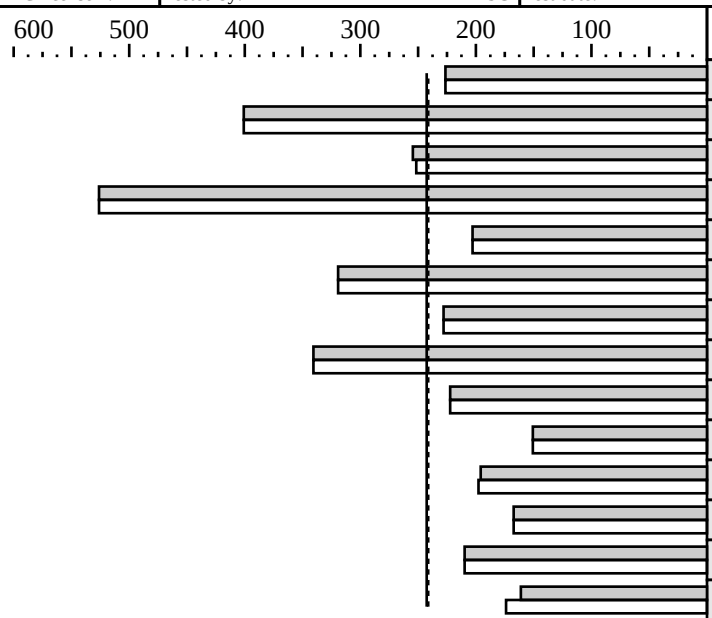
SGI

SGI Altix 350 (1400MHz/1.5MB, Itanium 2)

SPECfp_rate2000 = 243

SPECfp_rate_base2000 = 241

SPEC license #: 4 | Tested by: SGI | Test date: Jan-2004 | Hardware Avail: Jan-2004 | Software Avail: Dec-2003



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	16	131	226	16	131	226
171.swim	16	144	401	16	144	401
172.mgrid	16	131	255	16	133	252
173.applu	16	74.1	526	16	74.1	526
177.mesa	16	128	203	16	128	203
178.galgel	16	169	319	16	169	319
179.art	16	212	228	16	212	228
183.equake	16	70.8	341	16	70.8	341
187.facerec	16	159	222	16	159	222
188.ammp	16	271	151	16	271	151
189.lucas	16	190	196	16	188	198
191.fma3d	16	233	167	16	233	167
200.sixtrack	16	97.3	210	16	97.3	210
301.apsi	16	300	161	16	278	174

Hardware

CPU:	Intel Itanium 2
CPU MHz:	1400
FPU:	Integrated
CPU(s) enabled:	16 cores, 16 chips, 1 core/chip
CPU(s) orderable:	2-16
Parallel:	No
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	1.5MB (on chip) per CPU
Other Cache:	N/A
Memory:	32 GB (8*512MB PC2100 DIMMS per 2cpu module)
Disk Subsystem:	1 x 36 GB SCSI (Seagate Cheetah 15k rpm)
Other Hardware:	None

Software

Operating System:	SGI ProPack(TM) v2.3
Compiler:	Intel(R) Fortran Compiler for Linux 8.0 (Build 20031017) Intel(R) C++ Compiler for Linux 8.0 (Build 20031017)
File System:	xfs
System State:	Single-user

Notes/Tuning Information

```
+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -ipo -O3 +FDO -ansi_alias
  Fortran programs: -ipo -O3 +FDO

Portability Flags:
  178.galgel:  -FI

Peak optimization flags:
  168.wupwise: basepeak=yes
  171.swim: basepeak=yes
  172.mgrid: -O3 -ipo
  173.applu: basepeak=yes
  177.mesa: basepeak=yes
  178.galgel: basepeak=yes
  179.art: basepeak=yes
```

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Notes/Tuning Information (Continued)

```
    183.equake: basepeak=yes
    187.facerec: basepeak=yes
    188.ammp: basepeak=yes
    189.lucas: -O3 -ipo
    191.fma3d: basepeak=yes
    200.sixtrack: basepeak=yes
    301.apsi: -O3 -ipo

  Processes were bound to CPUs using dplace.
```