CFP2000 Result

Copyright ©1999-2004, Standard Performance Evaluation Corporation

Supermicro

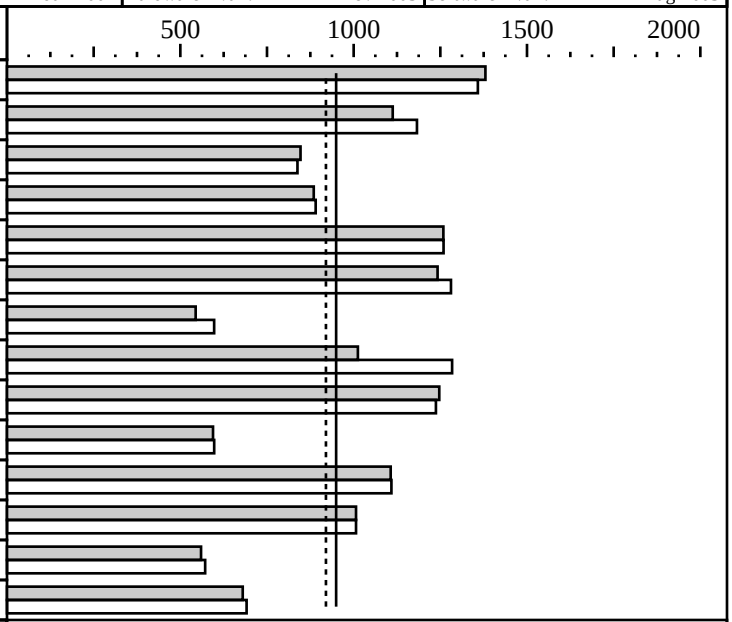
SUPER X5DPA-GG (3.06GHz, 533MHz bus)

SPECfp2000 = 949

SPECfp_base2000 = 920

SPEC license #01176 | Tested by: Supermicro computer, Inc., San Jose, CA | Test date: Jan-2004 | Hardware Avail: Nov-2003 | Software Avail: Aug-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	116	1380	118	1358
171.swim	3100	279	1113	262	1183
172.mgrid	1800	213	847	215	838
173.applu	2100	237	885	236	890
177.mesa	1400	111	1259	111	1259
178.galgel	2900	233	1242	226	1281
179.art	2600	478	544	435	598
183.equake	1300	128	1012	101	1284
187.facerec	1900	152	1247	154	1237
188.ammp	2200	370	594	368	598
189.lucas	2000	181	1107	180	1109
191.fma3d	2100	209	1007	209	1007
200.sixtrack	1100	197	560	192	572
301.apsi	2600	382	680	376	691



Hardware

CPU:	Intel Xeon (3.06GHz, 533MHz bus)
CPU MHz:	3066
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip (Hyper-Threading Technology enabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 8KB(D) on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x1024MB PC2100 Registered ECC Double-sided CL2
Disk Subsystem:	1 x IBM Deskstar 123.5GB IDE
Other Hardware:	

Software

Operating System:	Windows 2000 XP Professional W/ SP1a
Compiler:	Intel C++ and Fortran Compiler 7.1 Build 20030224Z Microsoft Visual Studio Enterprise Edition 6.0(for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning: -QxW -Qipo -O3 +FDO
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise:    -Qipo -QxW  +FDO
171.swim:       -Qipo -QxW  -O3 +FDO
172.mgrid:      -Qipo -QaxW -O3 +FDO
173.applu:      -Qipo -QxW  -O3 -Qscalar_rep- +FDO
177.mesa:       -Qipo -QxW  -O3 +FDO
178.galgel:     -Qipo -QxW  -O3 +FDO
179.art:        -Qipo -Oa -Qunroll4 -Zp4
183.equake:     -Qipo -QxW  -Oa +FDO shlw32M.lib
187.facerec:    -Qipo -QxW  -O3 -Qunroll1 -Qscalar_rep- +FDO
188.ammp:       -QxW  -Oa
189.lucas:      -Qipo -QaxW
191.fma3d:      basepeak=1
```

Copyright ©1999-2004, Standard Performance Evaluation Corporation

Notes/Tuning Information (Continued)

```
 200.sixtrack:   -Qipo -QxW  +FDO
 301.apsi:       -Qipo -QxW  -O3 +FDO
 Hyper-Threading Technology is turned ON
 One Processor installed
 ONESTEP is used for all base and peak runs
 The tested system can be assembled using an 1U Chassis such as the Supermicro SC811i-350,
with 350W power supply.  See Link for more chassis compatibility lists at
http://www.supermicro.com/PRODUCT/Chassis/compatibility.htm.
The System bus runs up to 8GB ECC registered DDR-200/266 memory in 4 DIMM slots.
 shlW32M.lib is the SmartHeap library V7.0 from MicroQuill www.microquill.com
```