CINT2000 Result

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Supermicro

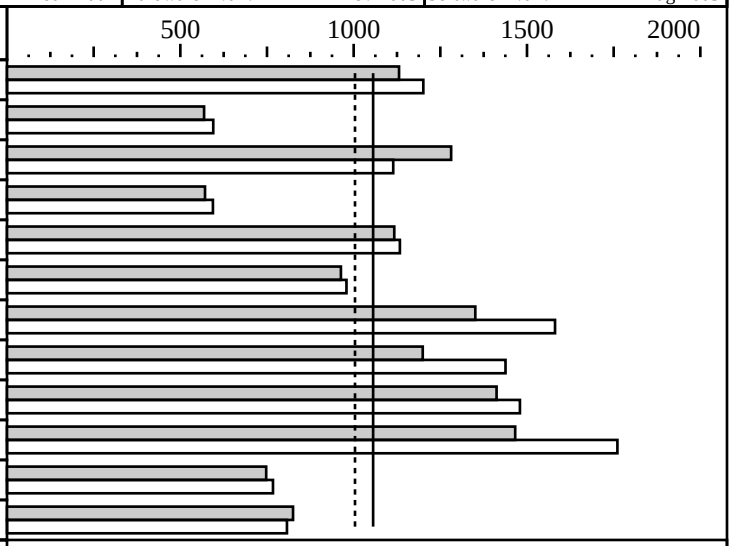
SUPER X5DPA-GG (3.06GHz, 533MHz bus)

SPECint2000 = 1056

SPECint_base2000 = 1004

SPEC license #01176 | Tested by: Supermicro computer, Inc., San Jose, CA | Test date: Jan-2004 | Hardware Avail: Nov-2003 | Software Avail: Aug-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	124	1131	117	1201
175.vpr	1400	246	569	235	595
176.gcc	1100	85.9	1281	98.7	1114
181.mcf	1800	315	571	303	594
186.crafty	1000	89.5	1117	88.2	1133
197.parser	1800	187	963	184	979
252.eon	1300	96.2	1351	82.2	1581
253.perlbmk	1800	150	1199	125	1438
254.gap	1100	77.9	1412	74.3	1480
255.vortex	1900	130	1466	108	1761
256.bzip2	1500	201	748	196	767
300.twolf	3000	364	825	371	808



Hardware

CPU:	Intel Xeon (3.06GHz, 533MHz bus)
CPU MHz:	3066
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip (Hyper-Threading Technology enabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 8KB(D) on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x1024MB PC2100 Registered ECC Double-sided CL2
Disk Subsystem:	1 x IBM Deskstar 123.5GB IDE
Other Hardware:	

Software

Operating System:	Windows XP Professional W/ SP1a
Compiler:	Intel C++ Compiler 7.1 Build 20030801Z Microsoft Visual Studio Enterprise Edition 6.0(for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -QxW -Qipo +FDO shlw32m.lib
Base tuning for C++ programs: -Qipo -GX -GR +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:  -O3 -QxW -Qipo +FDO
175.vpr:  -QxW -Qipo -O3 +FDO
176.gcc:  -QxW -Qipo -Oi- -O3 +FDO shlw32M.lib
181.mcf:  -Oa -Qipo +FDO shlw32M.lib
186.crafty:  -QxW -Qipo -O3 +FDO
197.parser:  -QxW -Qipo -Oa -O3 +FDO
252.eon: -QxW -Qipo -O3 +FDO
253.perlbmk: -Qipo -Oa -O3 +FDO shlw32M.lib
254.gap: -QxW -Qipo -O3 +FDO
255.vortex: -QxW -Qipo -O3 +FDO
256.bzip2: -Qunroll1 -Oa -Qipo +FDO
```

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Notes/Tuning Information (Continued)

300.twolf: -Qxi -Qipo +FDO shlw32M.lib
Hyper-Threading Technology is turned ON
One Processor installed
ONESTEP is used for all base and peak runs
The tested system can be assembled using an 1U Chassis such as the Supermicro SC811i-350, with 350W power supply. See Link for more chassis compatibility lists at http://www.supermicro.com/PRODUCT/Chassis/compatibility.htm.
The System bus runs up to 8GB ECC registered DDR-200/266 memory in 4 DIMM slots.
shlW32M.lib is the SmartHeap library V7.0 from MicroQuill www.microquill.com