CINT2000 Result

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Supermicro

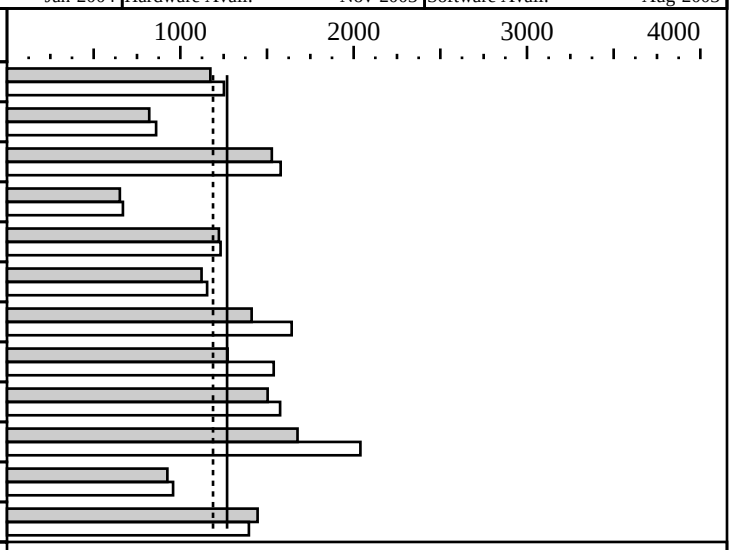
SUPER X5DPA-GG (3.2GHz, 533MHz bus)

SPECint2000 = 1270

SPECint_base2000 = 1189

SPEC license #01176 | Tested by: Supermicro computer, Inc., San Jose, CA | Test date: Jan-2004 | Hardware Avail: Nov-2003 | Software Avail: Aug-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	119	1173	112	1252
175.vpr	1400	171	821	163	860
176.gcc	1100	72.0	1528	69.7	1579
181.mcf	1800	276	651	269	669
186.crafty	1000	81.8	1223	81.2	1232
197.parser	1800	160	1122	156	1155
252.eon	1300	92.1	1411	79.2	1642
253.perlbmk	1800	141	1273	117	1539
254.gap	1100	73.2	1504	69.8	1576
255.vortex	1900	113	1676	93.2	2039
256.bzip2	1500	162	925	157	958
300.twolf	3000	208	1446	215	1396



Hardware

CPU:	Intel Xeon (3.2GHz, 533MHz bus)
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip (Hyper-Threading Technology enabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 8KB(D) on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x1024MB PC2100 Registered ECC Double-sided CL2
Disk Subsystem:	1 x IBM Deskstar 123.5GB IDE
Other Hardware:	

Software

Operating System:	Windows XP Professional W/ SP1a
Compiler:	Intel C++ Compiler 7.1 Build 20030801Z Microsoft Visual Studio Enterprise Edition 6.0(for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning for C programs:   -QxW -Qipo +FDO shlw32m.lib
Base tuning for C++ programs: -Qipo -GX -GR +FDO
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafy: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip:  -O3 -QxW -Qipo +FDO
175.vpr:  -O3 -QxW -Qipo +FDO
176.gcc:  -QxW -Qipo -Oi- -O3 +FDO shlw32M.lib
181.mcf:  -Oa -Qipo +FDO shlw32M.lib
186.crafty:  -QxW -Qipo -O3 +FDO
197.parser:  -QxW -Qipo -Oa -O3 +FDO
252.eon: -QxW -Qipo -O3 +FDO
253.perlbmk: -Qipo -Oa -O3 +FDO shlw32M.lib
254.gap: -QxW -Qipo -O3 +FDO
255.vortex: -QxW -Qipo -O3 +FDO
256.bzip2: -Qunroll1 -Oa -Qipo +FDO
```

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Notes/Tuning Information (Continued)

300.twolf: -Qxi -Qipo +FDO shlw32M.lib
Hyper-Threading Technology is turned ON
One Processor installed
ONESTEP is used for all base and peak runs
The tested system can be assembled using an 1U Chassis such as the Supermicro SC811i-350, with 350W power supply. See Link for more chassis compatibility lists at http://www.supermicro.com/PRODUCT/Chassis/compatibility.htm.
The System bus runs up to 8GB ECC registered DDR-200/266 memory in 4 DIMM slots.
shlW32M.lib is the SmartHeap library V7.0 from MicroQuill www.microquill.com