CFP2000 Result

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Supermicro

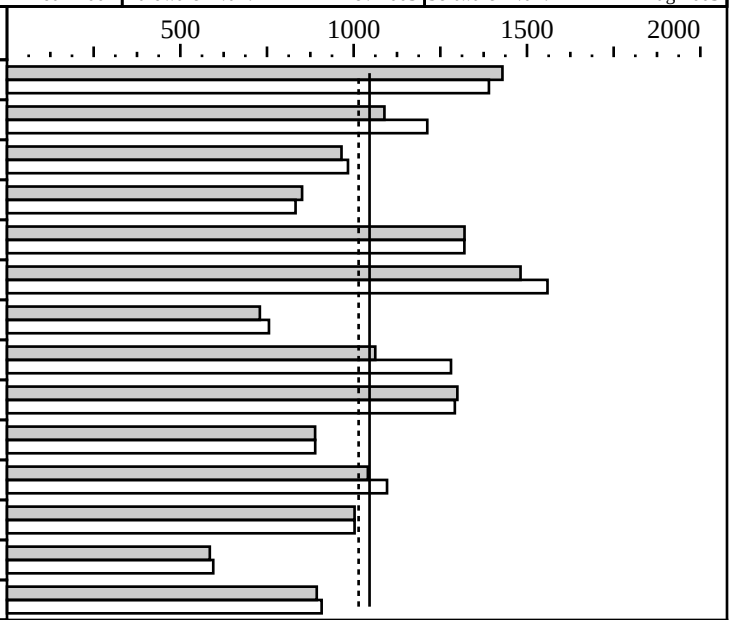
SUPER X5DPA-GG (3.2GHz, 533MHz bus)

SPECfp2000 = 1046

SPECfp_base2000 = 1014

SPEC license #01176 | Tested by: Supermicro computer, Inc., San Jose, CA | Test date: Jan-2004 | Hardware Avail: Nov-2003 | Software Avail: Aug-2003

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	112	1429	115	1390
171.swim	3100	285	1089	256	1212
172.mgrid	1800	187	965	183	984
173.applu	2100	247	851	252	832
177.mesa	1400	106	1320	106	1319
178.galgel	2900	196	1481	186	1559
179.art	2600	356	729	344	756
183.equake	1300	122	1062	102	1281
187.facerec	1900	146	1299	147	1292
188.ammp	2200	248	889	247	889
189.lucas	2000	192	1041	182	1096
191.fma3d	2100	209	1003	209	1003
200.sixtrack	1100	188	585	185	595
301.apsi	2600	291	894	286	908



Hardware

CPU:	Intel Xeon (3.2GHz, 533MHz bus)
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip (Hyper-Threading Technology enabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	12K(I) micro-ops + 8KB(D) on chip
Secondary Cache:	512KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x1024MB PC2100 Registered ECC Double-sided CL2
Disk Subsystem:	1 x IBM Deskstar 123.5GB IDE
Other Hardware:	

Software

Operating System:	Windows 2000 XP Professional W/ SP1a
Compiler:	Intel C++ and Fortran Compiler 7.1 Build 20030224Z Microsoft Visual Studio Enterprise Edition 6.0(for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
Base tuning: -QxW -Qipo -O3 +FDO
Portability:
178.galgel: -FI /F32000000
Peak tuning:
168.wupwise:    -Qipo -QxW  +FDO
171.swim:       -Qipo -QxW  -O3 +FDO
172.mgrid:      -Qipo -QaxW -O3 +FDO
173.applu:      -Qipo -QxW  -O3 -Qscalar_rep- +FDO
177.mesa:       -Qipo -QxW  -O3 +FDO
178.galgel:     -Qipo -QxW  -O3 +FDO
179.art:        -Qipo -Oa -Qunroll4 -Zp4
183.equake:     -Qipo -QxW  -Oa +FDO shlw32M.lib
187.facerec:    -Qipo -QxW  -O3 -Qunroll1 -Qscalar_rep- +FDO
188.ammp:       -QxW  -Oa
189.lucas:      -Qipo -QaxW
191.fma3d:      basepeak=1
```

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Notes/Tuning Information (Continued)

```
 200.sixtrack:   -Qipo -QxW  +FDO
 301.apsi:       -Qipo -QxW  -O3 +FDO
 Hyper-Threading Technology is turned ON
 One Processor installed
 ONESTEP is used for all base and peak runs
 The tested system can be assembled using an 1U Chassis such as the Supermicro SC811i-350,
with 350W power supply.  See Link for more chassis compatibility lists at
http://www.supermicro.com/PRODUCT/Chassis/compatibility.htm.
The System bus runs up to 8GB ECC registered DDR-200/266 memory in 4 DIMM slots.
 shlW32M.lib is the SmartHeap library V7.0 from MicroQuill www.microquill.com
```