CINT2000 Result

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Hewlett-Packard Company

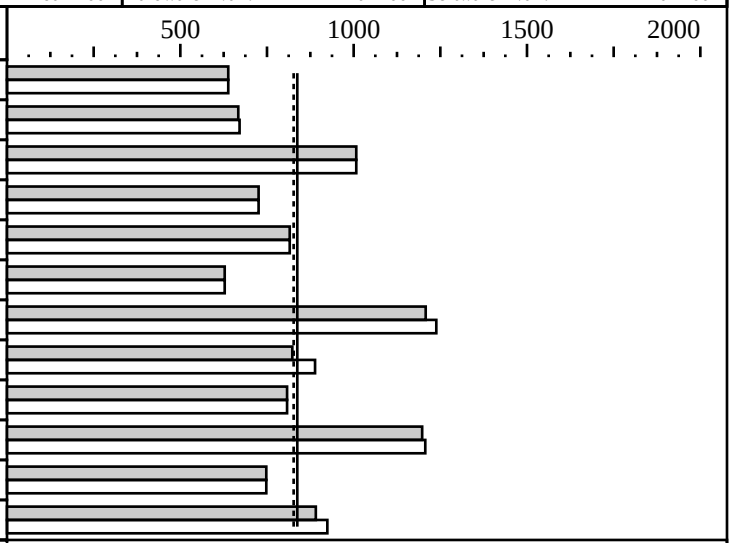
HP Integrity Server rx1600 (1000 MHz, Itanium 2)

SPECint2000 = 837

SPECint_base2000 = 827

SPEC license #: 3 | Tested by: HP Richardson | Test date: Jan-2004 | Hardware Avail: Mar-2004 | Software Avail: Mar-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	219	638	219	638
175.vpr	1400	210	667	209	671
176.gcc	1100	109	1008	109	1008
181.mcf	1800	248	726	248	726
186.crafty	1000	123	816	123	816
197.parser	1800	287	628	287	628
252.eon	1300	108	1208	105	1238
253.perlbmk	1800	219	823	203	889
254.gap	1100	136	808	136	808
255.vortex	1900	159	1198	157	1207
256.bzip2	1500	201	748	201	748
300.twolf	3000	337	891	325	924



Hardware

CPU:	Intel Itanium 2
CPU MHz:	1000
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1-2
Parallel:	no
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	1.5MB (on chip) per CPU
Other Cache:	N/A
Memory:	8GB (8 * 1GB DIMMs)
Disk Subsystem:	1x147GB 10k RPM SCSI disk
Other Hardware:	N/A

Software

Operating System:	HPUX11i-TCOE B.11.23
Compiler:	HP C/ANSI C Compiler B.11.23 (C.05.55) HP aC++ Compiler B.11.23 (C.05.55)
File System:	VxFS
System State:	Multi-user

Notes/Tuning Information

```
  Portability Flags
  176.gcc    : -DHOST_WORDS_BIG_ENDIAN
  186.crafty : -DHP
  252.eon    : -DFMAX_IS_DOUBLE
  253.perlbmk: -DSPEC_CPU2000_HP
  254.gap    : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
               -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO

 Base Flags
  PBO        : +Oprofile=collect:all/+Oprofile=use
  C          : PBO +Ofaster -exec +Odatalayout
  C++        : PBO +Ofaster +inline_level 2 -minshared

 Peak Flags
  164.gzip   : basepeak=true
  175.vpr    : PBO +Ofaster +Odatalayout +Onorecovery
                 +Onoptrs_to_globals -exec
               LIBS = -Wl,-aarchive_shared -Bprotected
  176.gcc    : basepeak=true
  181.mfc    : basepeak=true
```

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Notes/Tuning Information (Continued)

```
186.crafty : basepeak=true
197.parser : basepeak=true
252.eon    : PBO +Ofaster +Odatalayout +Otype_safety=ansi
               +inline_level 2 -minshared
253.perlbmk: PBO +Ofaster +Odatalayout +Onoptrs_to_globals -exec
             LIBS = -Wl,-aarchive_shared -Bprotected_def fastmem.o
254.gap    : basepeak=true
255.vortex : PBO +Ofaster +Odatalayout +Onorecovery
               +Onoptrs_to_globals -exec
             LIBS = -Wl,-aarchive_shared -Bprotected
256.bzip2  : basepeak=true
300.twolf  : PBO +Ofaster +Odatalayout +Onoptrs_to_globals -exec
             LIBS = -Wl,-aarchive_shared -Bprotected_data effmem.o

Kernel Tunables:
  maxdsiz            0x7b03a000
  maxdsiz_64bit   0x04000000000
  maxssiz            0x10000000
  maxssiz_64bit      0x40000000
  vps_ceiling             16384
  vps_pagesize             4096
  swapmem_on                  0
  dbc_max_pct                20
  dbc_min_pct                20

Note:
  EFI shell utility 'cpuconfig' used
  to deconfigure processors if necessary
```