CINT2000 Result

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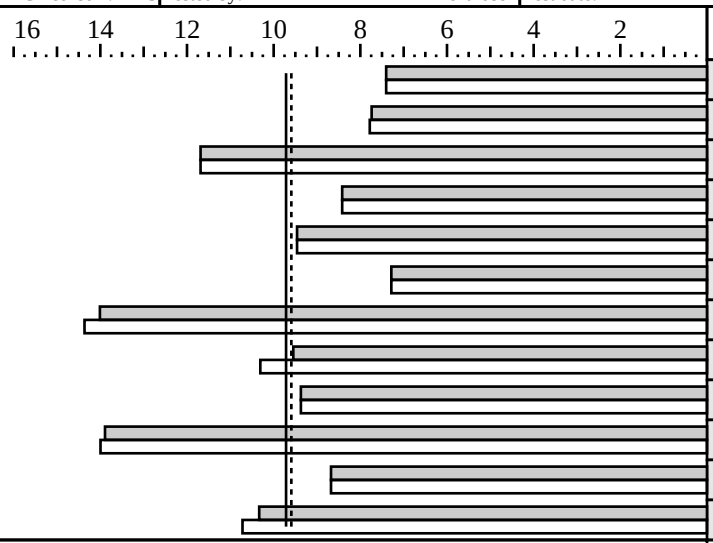
Hewlett-Packard Company

HP Integrity Server rx1600 (1000 MHz, Itanium 2)

SPECint_rate2000 = 9.71

SPECint_rate_base2000 = 9.59

SPEC license #: 3 | Tested by: HP Richardson | Test date: Jan-2004 | Hardware Avail: Mar-2004 | Software Avail: Mar-2004



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	1	219	7.40	1	219	7.40
175.vpr	1	210	7.74	1	209	7.78
176.gcc	1	109	11.7	1	109	11.7
181.mcf	1	248	8.42	1	248	8.42
186.crafty	1	123	9.46	1	123	9.46
197.parser	1	287	7.28	1	287	7.28
252.eon	1	108	14.0	1	105	14.4
253.perlbmk	1	219	9.55	1	203	10.3
254.gap	1	136	9.37	1	136	9.37
255.vortex	1	159	13.9	1	157	14.0
256.bzip2	1	201	8.68	1	201	8.68
300.twolf	1	337	10.3	1	325	10.7

Hardware

CPU:	Intel Itanium 2
CPU MHz:	1000
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1-2
Parallel:	no
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	1.5MB (on chip) per CPU
Other Cache:	N/A
Memory:	8GB (8 * 1GB DIMMs)
Disk Subsystem:	1x147GB 10k RPM SCSI disk
Other Hardware:	N/A

Software

Operating System:	HPUX11i-TCOE B.11.23
Compiler:	HP C/ANSI C Compiler B.11.23 (C.05.55) HP aC++ Compiler B.11.23 (C.05.55)
File System:	VxFS
System State:	Multi-user

Notes/Tuning Information

```
 Portability Flags
 176.gcc    : -DHOST_WORDS_BIG_ENDIAN
 186.crafty : -DHP
 252.eon    : -DFMAX_IS_DOUBLE
 253.perlbmk: -DSPEC_CPU2000_HP
 254.gap    : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
              -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO

Base Flags
 PBO        : +Oprofile=collect:all/+Oprofile=use
 C          : PBO +Ofaster -exec +Odatalayout
 C++        : PBO +Ofaster +inline_level 2 -minshared

Peak Flags
 164.gzip   : basepeak=true
 175.vpr    : PBO +Ofaster +Odatalayout +Onorecovery
                +Onoptrs_to_globals -exec
              LIBS = -Wl,-aarchive_shared -Bprotected
 176.gcc    : basepeak=true
 181.mfc    : basepeak=true
```

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Notes/Tuning Information (Continued)

```
186.crafty : basepeak=true
197.parser : basepeak=true
252.eon    : PBO +Ofaster +Odatalayout +Otype_safety=ansi
               +inline_level 2 -minshared
253.perlbmk: PBO +Ofaster +Odatalayout +Onoptrs_to_globals -exec
             LIBS = -Wl,-aarchive_shared -Bprotected_def fastmem.o
254.gap    : basepeak=true
255.vortex : PBO +Ofaster +Odatalayout +Onorecovery
               +Onoptrs_to_globals -exec
             LIBS = -Wl,-aarchive_shared -Bprotected
256.bzip2  : basepeak=true
300.twolf  : PBO +Ofaster +Odatalayout +Onoptrs_to_globals -exec
             LIBS = -Wl,-aarchive_shared -Bprotected_data effmem.o

Kernel Tunables:
  maxdsiz            0x7b03a000
  maxdsiz_64bit   0x04000000000
  maxssiz            0x10000000
  maxssiz_64bit      0x40000000
  vps_ceiling             16384
  vps_pagesize             4096
  swapmem_on                  0
  dbc_max_pct                20
  dbc_min_pct                20

Note:
  EFI shell utility 'cpuconfig' used
  to deconfigure processors if necessary
```