CINT2000 Result

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Hewlett-Packard Company

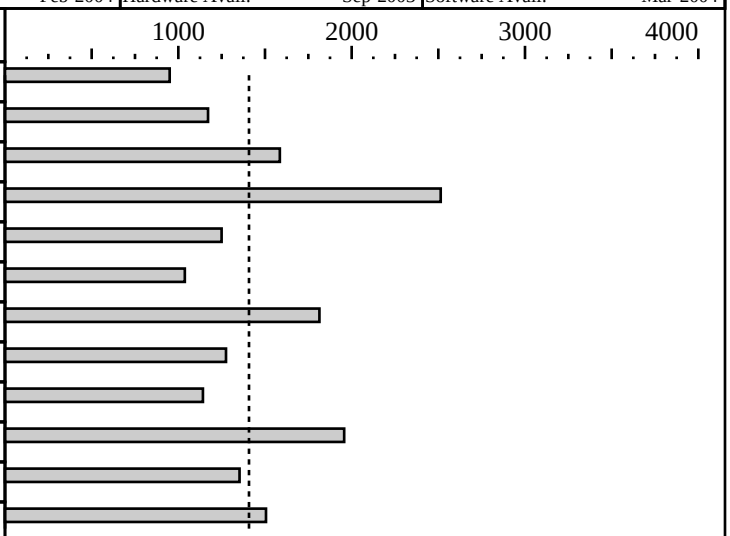
HP Integrity Server rx2600 (1500 MHz, Itanium 2)

SPECint2000 = --

SPECint_base2000 = 1408

SPEC license #: 3 | Tested by: HP Richardson | Test date: Feb-2004 | Hardware Avail: Sep-2003 | Software Avail: Mar-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	147	950		
175.vpr	1400	120	1171		
176.gcc	1100	69.4	1586		
181.mcf	1800	71.6	2514		
186.crafty	1000	80.0	1250		
197.parser	1800	174	1037		
252.eon	1300	71.7	1814		
253.perlbmk	1800	141	1275		
254.gap	1100	96.3	1142		
255.vortex	1900	97.1	1957		
256.bzip2	1500	111	1353		
300.twolf	3000	199	1506		



Hardware

CPU:	Intel Itanium 2
CPU MHz:	1500
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1-2
Parallel:	no
Primary Cache:	16KBI + 16KBD (on chip) per CPU
Secondary Cache:	256KB (on chip) per CPU
L3 Cache:	6.0MB (on chip) per CPU
Other Cache:	N/A
Memory:	12GB (12 x 1024MB DIMMs)
Disk Subsystem:	1x36GB 15k RPM SCSI disk
Other Hardware:	N/A

Software

Operating System:	HPUX11i-TCOE B.11.23
Compiler:	HP C/ANSI C Compiler C.05.55
	HP aC++ Compiler C.05.55
	PHSS_29658 Aries
	PHSS_29678 Math Library Patch
	PHSS_30018 aC++ comp patch
	PHSS_30019 C/ANSIC comp patch
	PHSS_30020 u2comp/be/plugin patch
	PHSS_30230 linker + fdp patch
File System:	vxfs
System State:	Multi-user

Notes/Tuning Information

```
  Portability Flags
  176.gcc    : -DHOST_WORDS_BIG_ENDIAN
  186.crafty : -DHP
  252.eon    : -DFMAX_IS_DOUBLE
  253.perlbmk: -DSPEC_CPU2000_HP
  254.gap    : -DSPEC_CPU2000_HP -DSYS_IS_USG -DSYS_HAS_IOCTL_PROTO
               -DSYS_HAS_TIME_PROTO -DSYS_HAS_CALLOC_PROTO

 Base Flags
  PBO        : +Oprofile=collect:all/+Oprofile=use
  C          : PBO +Ofaster -exec +Odatalayout
  C++        : PBO +Ofaster +inline_level 2 -minshared

  Kernel Tunables:
    maxdsiz            0x7b03a000
    maxdsiz_64bit   0x04000000000
    maxssiz            0x10000000
    maxssiz_64bit      0x40000000
    vps_ceiling             16384
    vps_pagesize             4096
```

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Notes/Tuning Information (Continued)

```
    swapmem_on                  0
    dbc_max_pct                20
    dbc_min_pct                20

  Note:
    EFI shell utility 'cpuconfig' used
    to deconfigure processors
```