CINT2000 Result

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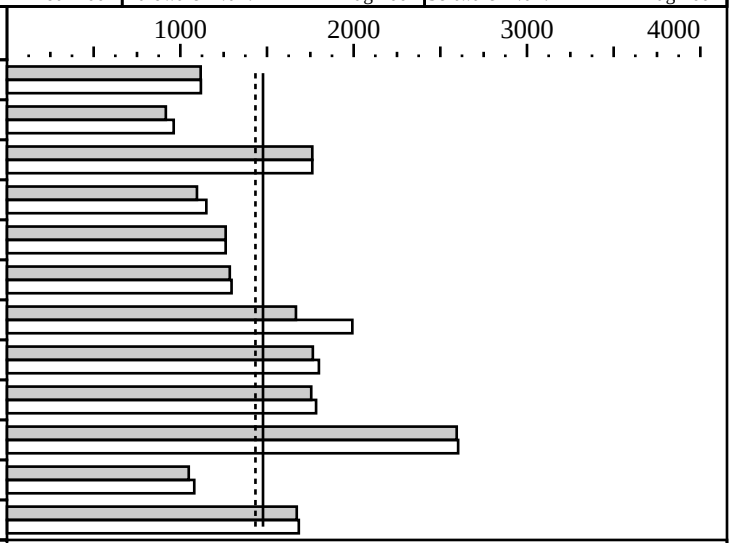
Hewlett-Packard Company
ProLiant DL380 G4 (3.4GHz, Intel Xeon)

SPECint2000 = 1477

SPECint_base2000 = 1433

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jul-2004 | Hardware Avail: Aug-2004 | Software Avail: Aug-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
164.gzip	1400	125	1117	125	1119
175.vpr	1400	153	916	146	962
176.gcc	1100	62.5	1761	62.5	1761
181.mcf	1800	164	1096	157	1150
186.crafty	1000	79.3	1262	79.3	1262
197.parser	1800	140	1285	139	1296
252.eon	1300	78.0	1667	65.3	1992
253.perlbmk	1800	102	1764	100	1799
254.gap	1100	62.7	1755	61.7	1783
255.vortex	1900	73.2	2594	73.0	2603
256.bzip2	1500	143	1048	139	1080
300.twolf	3000	179	1672	178	1683



Hardware

CPU:	Intel Xeon (3.4GHz, 1MB L2, 800MHz bus)
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip (HT Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	16K micro-ops I + 16KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4x512MB DDR2-400
Disk Subsystem:	1x18.2GB 15K Ultra320 SCSI
Other Hardware:	

Software

Operating System:	Windows Server 2003 Standard (Build 3790)
Compiler:	Intel C++ Compiler 8.0 Build 20031211Z Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
+FDO*: FDO (-QxP not used in PASS1 to enable cross-compilation)
Base tuning for C programs:   -QxP -fast +FDO* shlw32M.lib
Base tuning for C++ programs: -QxP -fast -GX -GR
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafy: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:   -QxP -fast -Oa           +FDO  ONESTEP=yes
  175.vpr:    -QxP -fast               +FDO  ONESTEP=yes
  176.gcc:    basepeak = 1
  181.mcf:    -QxP -fast               +FDO  ONESTEP=yes shlw32M.lib
  186.crafty: -QxP -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  197.parser: -QxP -fast               +FDO  ONESTEP=yes
  252.eon:    -QxP       -O2 -Qipo     +FDO*
  253.perlbmk:     -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  254.gap:    -QxP -fast               +FDO  ONESTEP=yes
  255.vortex: -QxP -fast               +FDO  ONESTEP=yes shlw32M.lib
```

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Notes/Tuning Information (Continued)

```
  256.bzip2:       -fast -Oa -Qunroll1 +FDO  ONESTEP=yes
  300.twolf:  -QxP -fast -O3           +FDO*             shlw32M.lib
BIOS Configuration Notes
  HT Technology disabled
```