CFP2000 Result

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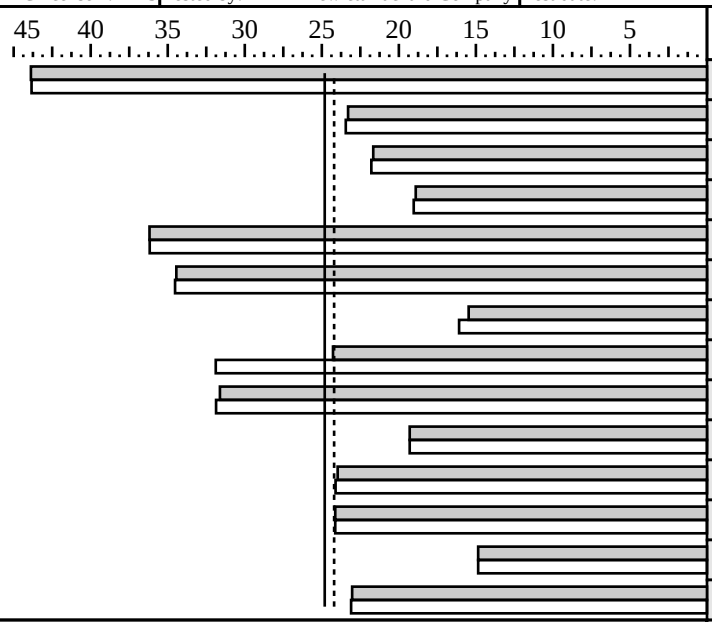
Hewlett-Packard Company
ProLiant DL380 G4 (3.6GHz, Intel Xeon)

SPECfp_rate2000 = 24.8

SPECfp_rate_base2000 = 24.2

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jul-2004 | Hardware Avail: Aug-2004 | Software Avail: Aug-2004



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	84.6	43.9	2	84.7	43.8
171.swim	2	309	23.3	2	307	23.4
172.mgrid	2	193	21.7	2	192	21.8
173.applu	2	258	18.9	2	256	19.0
177.mesa	2	89.8	36.2	2	89.8	36.2
178.galgel	2	195	34.4	2	195	34.5
179.art	2	390	15.5	2	375	16.1
183.equake	2	124	24.3	2	94.6	31.9
187.facerec	2	139	31.6	2	138	31.9
188.ammp	2	265	19.3	2	265	19.3
189.lucas	2	194	24.0	2	192	24.1
191.fma3d	2	202	24.1	2	202	24.1
200.sixtrack	2	172	14.9	2	172	14.9
301.apsi	2	262	23.0	2	261	23.1

Hardware

CPU:	Intel Xeon (3.6GHz, 1MB L2, 800MHz bus)
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	2 cores, 2 chips, 1 core/chip (HT Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	16K micro-ops I + 16KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4x512MB DDR2-400
Disk Subsystem:	1x18.2GB 15K Ultra320 SCSI
Other Hardware:	

Software

Operating System:	Windows Server 2003 Standard (Build 3790)
Compiler:	Intel C++ Compiler 8.0 Build 20031211Z Intel Fortran Compiler 8.0 build 20040125Z Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
+FDO*: FDO (-QxP not used in PASS1 to enable cross-compilation)

Base tuning for All Programs: -QxP -fast -Qansi_alias +FDO*
Portability flags:
  178.galgel: -FI /F32000000
Peak tuning:
  168.wupwise:    -QxP  -fast -Qansi_alias                          +FDO*
  171.swim:       -QxP  -fast -Qansi_alias                          +FDO  ONESTEP=yes
  172.mgrid:      -QxP  -fast -Qansi_alias                          +FDO*
  173.applu:      -QxP  -fast -Qscalar_rep- -Qauto -Qansi_alias     +FDO*
  177.mesa:       -QxP  -fast                                       +FDO* ONESTEP=yes
  178.galgel:     -QxP  -fast -Qansi_alias                          +FDO* ONESTEP=yes
  179.art:              -Oa   -Qipo -Qunroll4 -Zp4
  183.equake:     -QxP  -fast -Oa -Qrcd                    +FDO* ONESTEP=yes shlw32M.lib
  187.facerec:    -QaxP -fast -Qunroll1 -Qscalar_rep- -fpp          +FDO* ONESTEP=yes
  188.ammp:       basepeak=1
```

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Notes/Tuning Information (Continued)

```
    189.lucas:       -QxP  -fast -Qprefetch-                    +FDO*
    191.fma3d:       basepeak=1
    200.sixtrack:    -QxP  -fast                                +FDO*
    301.apsi:        -QxP  -fast                                +FDO*
  BIOS Configuration Notes
    HT Technology disabled
    Adjacent Sector Prefetch disabled
```