CFP2000 Result

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Hewlett-Packard Company

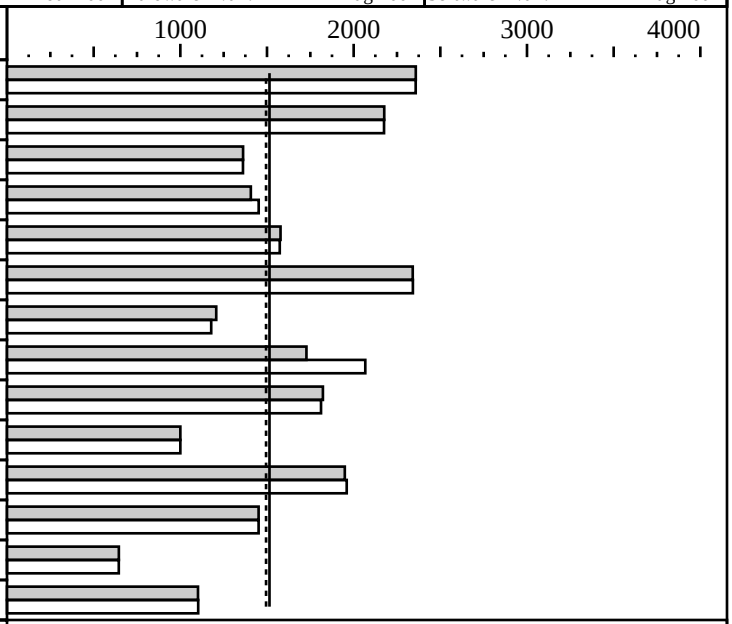
ProLiant DL380 G4 (3.6GHz, Intel Xeon)

SPECfp2000 = 1514

SPECfp_base2000 = 1495

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jul-2004 | Hardware Avail: Aug-2004 | Software Avail: Aug-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	67.8	2358	67.9	2358
171.swim	3100	142	2176	143	2175
172.mgrid	1800	132	1362	132	1361
173.applu	2100	149	1407	145	1452
177.mesa	1400	88.7	1578	89.0	1574
178.galgel	2900	124	2341	124	2342
179.art	2600	215	1207	221	1178
183.equake	1300	75.3	1728	62.9	2067
187.facerec	1900	104	1823	105	1812
188.ammp	2200	220	1000	220	1000
189.lucas	2000	103	1949	102	1960
191.fma3d	2100	145	1452	145	1452
200.sixtrack	1100	170	645	171	645
301.apsi	2600	236	1102	236	1103



Hardware

CPU:	Intel Xeon (3.6GHz, 1MB L2, 800MHz bus)
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip (HT Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	16K micro-ops I + 16KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	4x512MB DDR2-400
Disk Subsystem:	1x18.2GB 15K Ultra320 SCSI
Other Hardware:	

Software

Operating System:	Windows Server 2003 Standard (Build 3790)
Compiler:	Intel C++ Compiler 8.0 Build 20031211Z Intel Fortran Compiler 8.0 build 20040125Z Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
+FDO*: FDO (-QxP not used in PASS1 to enable cross-compilation)

Base tuning:  -QxP -fast -Qansi_alias +FDO*
Portability flags:
  178.galgel: -FI /F32000000
Peak tuning:
  168.wupwise:    -QxP  -fast -Qansi_alias                  +FDO*
  171.swim:       -QxP  -fast                       -f77rtl +FDO ONESTEP=yes
  172.mgrid:      -QxP  -fast -Qansi_alias                  +FDO*
  173.applu:      -QxP  -fast -Qscalar_rep- -Qauto -f77rtl +FDO ONESTEP=yes
  177.mesa:       -QxP  -fast                               +FDO ONESTEP=yes
  178.galgel:     -QxP  -fast                               +FDO ONESTEP=yes
  179.art:              -fast -Oa -Qunroll4 -Zp4                ONESTEP=yes
  183.equake:    -QxP  -fast -Oa -Qrcd                   +FDO ONESTEP=yes shlw32M.lib
  187.facerec:    -QaxN -fast -Qunroll1 -Qscalar_rep- -fpp +FDO ONESTEP=yes
  188.ammp:       basepeak=1
```

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Notes/Tuning Information (Continued)

```
    189.lucas:      -QaxN-fast  <typo, included for completeness>
    191.fma3d:      basepeak=1
    200.sixtrack:   -QxP  -fast                              +FDO*
    301.apsi:       -QxP  -fast                    -f77rtl +FDO ONESTEP=yes
  BIOS Configuration Notes
    HT Technology disabled
```