CFP2000 Result

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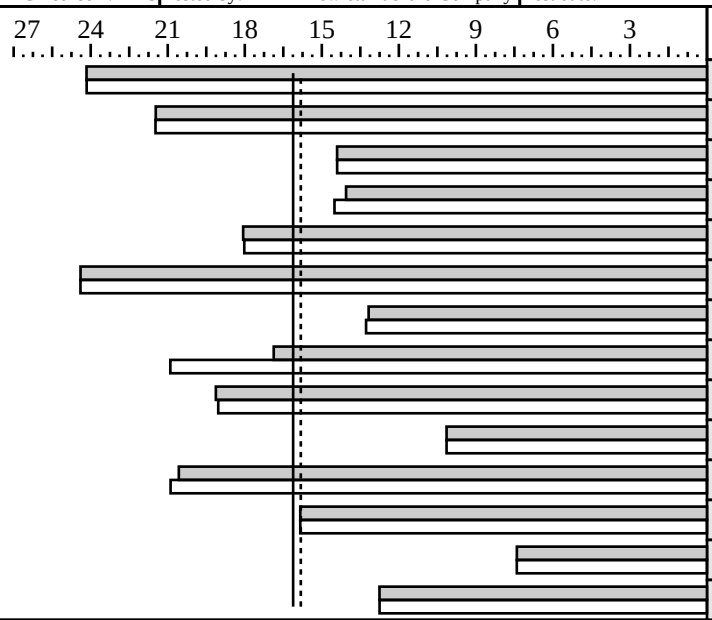
Hewlett-Packard Company
ProLiant DL360 G4 (3.6GHz, Intel Xeon)

SPECfp_rate2000 = 16.1

SPECfp_rate_base2000 = 15.8

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jul-2004 | Hardware Avail: Nov-2004 | Software Avail: Aug-2004



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	1	76.8	24.2	1	76.8	24.2
171.swim	1	168	21.5	1	167	21.5
172.mgrid	1	145	14.4	1	145	14.4
173.applu	1	173	14.1	1	168	14.5
177.mesa	1	89.9	18.1	1	90.1	18.0
178.galgel	1	138	24.4	1	138	24.4
179.art	1	229	13.2	1	227	13.3
183.equake	1	89.4	16.9	1	72.2	20.9
187.facerec	1	115	19.1	1	116	19.0
188.ammp	1	252	10.1	1	252	10.1
189.lucas	1	113	20.6	1	111	20.9
191.fma3d	1	154	15.8	1	154	15.8
200.sixtrack	1	172	7.40	1	172	7.41
301.apsi	1	236	12.8	1	237	12.8

Hardware

CPU:	Intel Xeon (3.6GHz, 1MB L2, 800MHz bus)
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip (HT Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	16K micro-ops I + 16KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x1024MB PC2700 DDR-333
Disk Subsystem:	1x230GB SATA
Other Hardware:	

Software

Operating System:	Windows Server 2003 Standard (Build 3790)
Compiler:	Intel C++ Compiler 8.0 Build 20031211Z Intel Fortran Compiler 8.0 build 20040125Z Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
+FDO*: FDO (-QxP not used in PASS1 to enable cross-compilation)

Base tuning for All Programs: -QxP -fast -Qansi_alias +FDO*
Portability flags:
  178.galgel: -FI /F32000000
Peak tuning:
  168.wupwise:    -QxP  -fast -Qansi_alias                          +FDO*
  171.swim:       -QxP  -fast                       -f77rtl         +FDO  ONESTEP=yes
  172.mgrid:      -QxP  -fast -Qansi_alias                          +FDO*
  173.applu:      -QxP  -fast -Qscalar_rep- -Qauto -f77rtl          +FDO  ONESTEP=yes
  177.mesa:       -QxP  -fast                                       +FDO  ONESTEP=yes
  178.galgel:     -QxP  -fast                                       +FDO  ONESTEP=yes
  179.art:              -fast -Oa       -Qunroll4 -Zp4                    ONESTEP=yes
  183.equake:     -QxP  -fast -Oa -Qrcd                       +FDO  ONESTEP=yes shlw32M.lib
  187.facerec:    -QaxN -fast -Qunroll1 -Qscalar_rep- -fpp          +FDO  ONESTEP=yes
  188.ammp:       basepeak=1
```

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Notes/Tuning Information (Continued)

```
    189.lucas:      -QxN-fast
    191.fma3d:      basepeak=1
    200.sixtrack:   -QxP  -fast                                  +FDO*
    301.apsi:       -QxP  -fast                      -f77rtl     +FDO
  BIOS Configuration Notes
    HT Technology disabled
    Adjacent Sector Prefetch disabled

Submitted_by: "Horton, Richard (Server Performance)" <richard.horton@hp.com>
Submitted: Mon Oct  4 18:32:37 2004
Submission: cpu2000-20041004-03423.sub
```