CFP2000 Result

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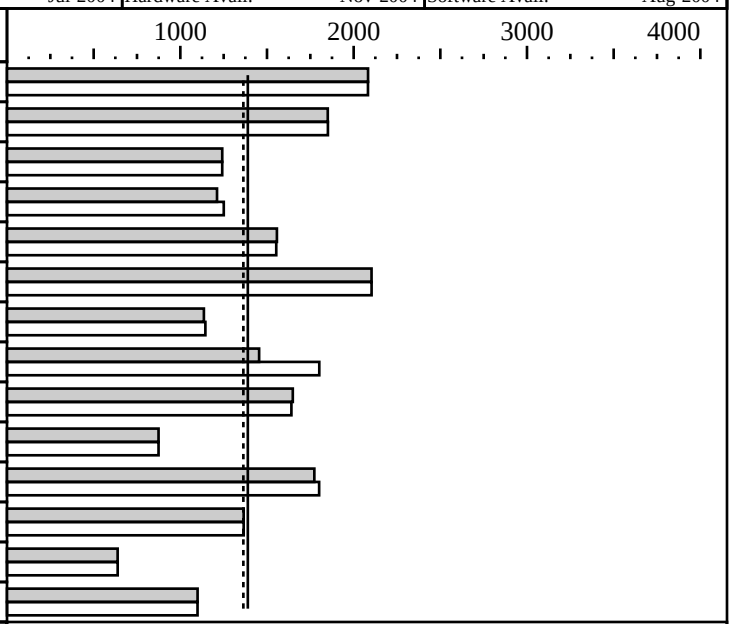
Hewlett-Packard Company
ProLiant DL360 G4 (3.6GHz, Intel Xeon)

SPECfp2000 = 1389

SPECfp_base2000 = 1364

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jul-2004 | Hardware Avail: Nov-2004 | Software Avail: Aug-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio
168.wupwise	1600	76.8	2083	76.8	2083
171.swim	3100	168	1851	167	1851
172.mgrid	1800	145	1242	145	1242
173.applu	2100	173	1212	168	1251
177.mesa	1400	89.9	1557	90.1	1553
178.galgel	2900	138	2103	138	2103
179.art	2600	229	1136	227	1145
183.equake	1300	89.4	1455	72.2	1802
187.facerec	1900	115	1649	116	1641
188.ammp	2200	252	874	252	874
189.lucas	2000	113	1773	111	1801
191.fma3d	2100	154	1365	154	1365
200.sixtrack	1100	172	638	172	638
301.apsi	2600	236	1100	237	1099



Hardware

CPU:	Intel Xeon (3.6GHz, 1MB L2, 800MHz bus)
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip (HT Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	16K micro-ops I + 16KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x1024MB PC2700 DDR-333
Disk Subsystem:	1x230GB SATA
Other Hardware:	

Software

Operating System:	Windows Server 2003 Standard (Build 3790)
Compiler:	Intel C++ Compiler 8.0 Build 20031211Z Intel Fortran Compiler 8.0 build 20040125Z Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
+FDO*: FDO (-QxP not used in PASS1 to enable cross-compilation)

Base tuning for All Programs: -QxP -fast -Qansi_alias +FDO*
Portability flags:
  178.galgel: -FI /F32000000
Peak tuning:
  168.wupwise:    -QxP  -fast -Qansi_alias                          +FDO*
  171.swim:       -QxP  -fast                       -f77rtl         +FDO  ONESTEP=yes
  172.mgrid:      -QxP  -fast -Qansi_alias                          +FDO*
  173.applu:      -QxP  -fast -Qscalar_rep- -Qauto -f77rtl          +FDO  ONESTEP=yes
  177.mesa:       -QxP  -fast                                       +FDO  ONESTEP=yes
  178.galgel:     -QxP  -fast                                       +FDO  ONESTEP=yes
  179.art:              -fast -Oa         -Qunroll4 -Zp4                  ONESTEP=yes
  183.equake:     -QxP  -fast -Oa -Qrcd                       +FDO  ONESTEP=yes shlw32M.lib
  187.facerec:    -QaxN -fast -Qunroll1 -Qscalar_rep- -fpp          +FDO  ONESTEP=yes
  188.ammp:       basepeak=1
```

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Notes/Tuning Information (Continued)

```
    189.lucas:       -QxN-fast
    191.fma3d:       basepeak=1
    200.sixtrack:    -QxP  -fast                                  +FDO*
    301.apsi:        -QxP  -fast                      -f77rtl     +FDO
  BIOS Configuration Notes
    HT Technology disabled
    Adjacent Sector Prefetch disabled
```