CINT2000 Result

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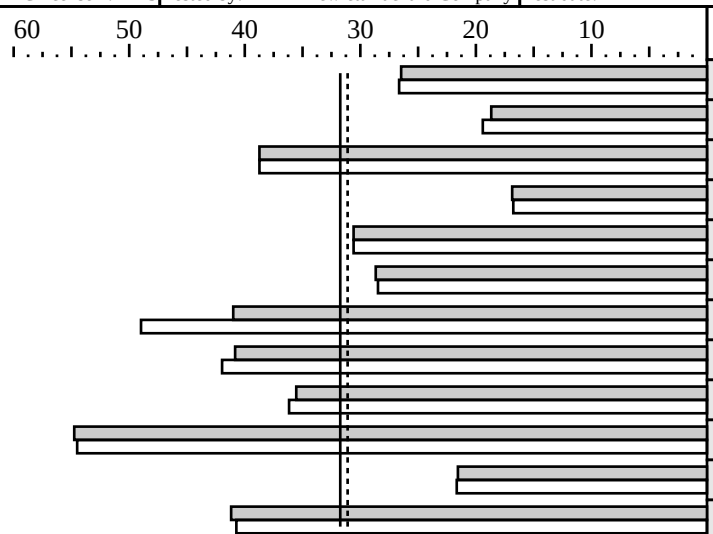
Hewlett-Packard Company
ProLiant DL360 G4 (3.6GHz, Intel Xeon)

SPECint_rate2000 = 31.7

SPECint_rate_base2000 = 31.1

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Jul-2004 | Hardware Avail: Nov-2004 | Software Avail: Aug-2004



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	123	26.5	2	122	26.6
175.vpr	2	174	18.7	2	167	19.4
176.gcc	2	65.9	38.7	2	65.9	38.7
181.mcf	2	248	16.9	2	249	16.8
186.crafty	2	75.9	30.6	2	75.9	30.6
197.parser	2	146	28.7	2	147	28.5
252.eon	2	73.6	41.0	2	61.6	49.0
253.perlbmk	2	102	40.8	2	99.5	42.0
254.gap	2	71.8	35.5	2	70.6	36.2
255.vortex	2	80.5	54.8	2	80.9	54.5
256.bzip2	2	161	21.6	2	161	21.7
300.twolf	2	169	41.2	2	171	40.7

Hardware

CPU:	Intel Xeon (3.6GHz, 1MB L2, 800MHz bus)
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	2 cores, 2 chips, 1 core/chip (HT Technology disabled)
CPU(s) orderable:	1,2
Parallel:	No
Primary Cache:	16K micro-ops I + 16KBD on chip
Secondary Cache:	1024KB(I+D) on chip
L3 Cache:	N/A
Other Cache:	N/A
Memory:	2x1024MB PC2700 DDR-333
Disk Subsystem:	1x230GB SATA
Other Hardware:	

Software

Operating System:	Windows Server 2003 Standard (Build 3790)
Compiler:	Intel C++ Compiler 8.0 Build 20031211Z Microsoft Visual Studio .NET 7.0.9466 (for libraries) MicroQuill Smartheap Library 7.0
File System:	NTFS
System State:	Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen  PASS2=-Qprof_use
+FDO*: FDO (-QxP not used in PASS1 to enable cross-compilation)

Base tuning for C programs:   -QxP -fast +FDO* shlw32M.lib
Base tuning for C++ programs: -QxP -fast -GX -GR
Portability flags:
  176.gcc: -Dalloca=_alloca /F10000000
  186.crafy: -DNT_i386
  253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
  254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
  164.gzip:    -QxP -fast -Oa           +FDO  ONESTEP=yes
  175.vpr:     -QxP -fast               +FDO  ONESTEP=yes
  176.gcc:     basepeak = 1
  181.mcf:     -QxP -fast               +FDO  ONESTEP=yes shlw32M.lib
  186.crafty:  -QxP -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  197.parser:  -QxP -fast               +FDO  ONESTEP=yes
  252.eon:     -QxP       -O2 -Qipo     +FDO*
  253.perlbmk:      -fast -Oa           +FDO  ONESTEP=yes shlw32M.lib
  254.gap:     -QxP -fast               +FDO  ONESTEP=yes
```

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Notes/Tuning Information (Continued)

```
    255.vortex:  -QxP -fast               +FDO  ONESTEP=yes shlw32M.lib
    256.bzip2:        -fast -Oa -Qunroll1 +FDO  ONESTEP=yes
    300.twolf:   -QxP -fast -O3           +FDO* shlw32M.lib [-O3 not in Pass 1]
  BIOS Configuration Notes
    HT Technology disabled
    Adjacent Sector Prefetch disabled
```