# CFP2000 Result

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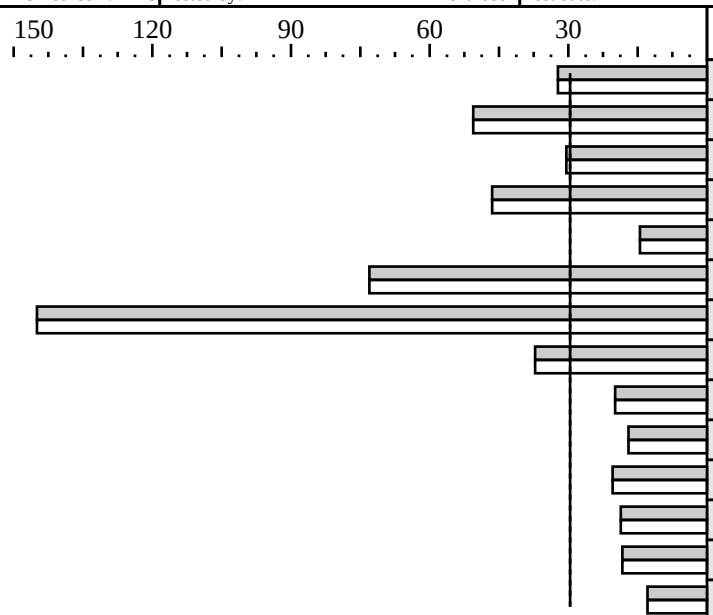
## Hewlett-Packard Company

HP Integrity rx2620-2 (1.6GHz/3MB Itanium 2)

SPECfp_rate2000 = 29.6

SPECfp_rate_base2000 = 29.6

SPEC license #: 3 | Tested by: HP Richardson | Test date: Oct-2004 | Hardware Avail: Dec-2004 | Software Avail: Nov-2004




| Benchmark | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|---|---|---|---|---|---|---|
| 168.wupwise | 1 | 57.5 | 32.3 | 1 | 57.5 | 32.3 |
| 171.swim | 1 | 71.1 | 50.6 | 1 | 71.1 | 50.6 |
| 172.mgrid | 1 | 68.6 | 30.4 | 1 | 68.6 | 30.4 |
| 173.applu | 1 | 52.4 | 46.5 | 1 | 52.4 | 46.5 |
| 177.mesa | 1 | 112 | 14.5 | 1 | 112 | 14.5 |
| 178.galgel | 1 | 46.1 | 73.0 | 1 | 46.1 | 73.0 |
| 179.art | 1 | 20.8 | 145 | 1 | 20.8 | 145 |
| 183.equake | 1 | 40.6 | 37.2 | 1 | 40.6 | 37.2 |
| 187.facerec | 1 | 111 | 19.9 | 1 | 111 | 19.9 |
| 188.ammp | 1 | 150 | 17.0 | 1 | 150 | 17.0 |
| 189.lucas | 1 | 114 | 20.4 | 1 | 114 | 20.4 |
| 191.fma3d | 1 | 131 | 18.6 | 1 | 131 | 18.6 |
| 200.sixtrack | 1 | 69.7 | 18.3 | 1 | 69.7 | 18.3 |
| 301.apsi | 1 | 234 | 12.9 | 1 | 234 | 12.9 |

### Hardware

| | |
|---|---|
| CPU: | Intel Itanium 2 |
| CPU MHz: | 1600 |
| FPU: | Integrated |
| CPU(s) enabled: | 1 core, 1 chip, 1 core/chip |
| CPU(s) orderable: | 1-2 |
| Parallel: | no |
| Primary Cache: | 16KBI + 16KBD on chip, per core |
| Secondary Cache: | 256KB (I+D) on chip, per core |
| L3 Cache: | 3.0MB (I+D) on chip, per core |
| Other Cache: | N/A |
| Memory: | 24GB (12x2GB DIMMs) |
| Disk Subsystem: | 1x146GB 10K SCSI |
| Other Hardware: | N/A |

### Software

| | |
|---|---|
| Operating System: | Red Hat Linux Advanced Server release 3.0AS (Update 3) |
| Compiler: | Intel C++ Compiler 8.1 for Linux (Build 20041021)<br>Intel Fortran Compiler 8.1 for Linux (Build 20041021) |
| File System: | ext3 |
| System State: | Multi-user |

## Notes/Tuning Information

```
Base tuning flags:
        C programs: -fast -ansi_alias +FDO -IPF_fp_relaxed
   Fortran programs: -fast +FDO -IPF_fp_relaxed

Portability:
        178.galgel:  -FI
        187.facerec: srcalt=AllocShape

Peak flags same as baseline (basepeak=true set globally)
+FDO: PASS1=-prof_gen  PASS2=-prof_use

Note:
  EFI command "cpuconfig" used prior to booting to
  deconfigure other processors if necessary
```