CINT2000 Result

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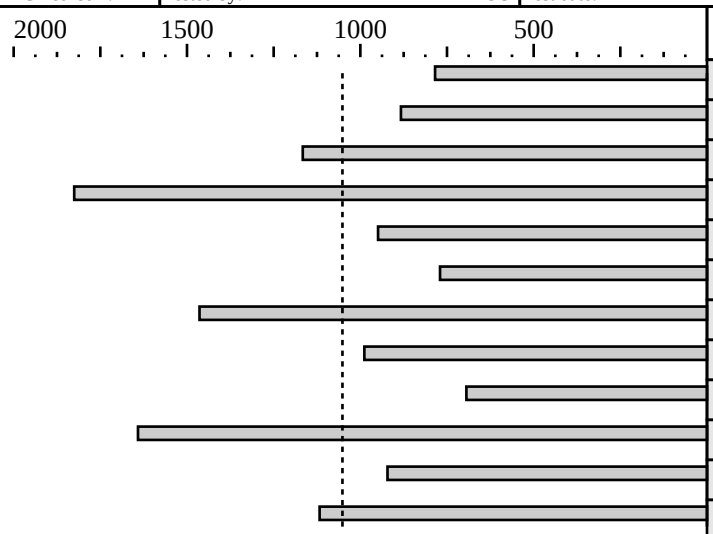
SGI

SGI Altix 3700 Bx2 (1600MHz 9M L3, Itanium 2)

SPECint_rate2000 = --

SPECint_rate_base2000 = 1052

SPEC license #: 4 | Tested by: SGI | Test date: Oct-2004 | Hardware Avail: Nov-2004 | Software Avail: Nov-2004



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	64	133	784			
175.vpr	64	118	883			
176.gcc	64	70.0	1166			
181.mcf	64	73.2	1825			
186.crafty	64	78.2	949			
197.parser	64	174	770			
252.eon	64	65.9	1464			
253.perlbmk	64	135	988			
254.gap	64	118	694			
255.vortex	64	85.9	1641			
256.bzip2	64	121	922			
300.twolf	64	199	1117			

Hardware

CPU:	Intel Itanium 2
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	64 cores, 64 chips, 1 core/chip
CPU(s) orderable:	16-256
Parallel:	No
Primary Cache:	16KBI + 16KBD on chip, per core
Secondary Cache:	256KB (I+D) on chip, per core
L3 Cache:	9.0MB (I+D) on chip, per core
Other Cache:	N/A
Memory:	256 GB (32*1GB PC2700 DIMMS per 8 core module)
Disk Subsystem:	16 x 73 GB FibreChannel (Seagate Cheetah 15k rpm)
Other Hardware:	None

Software

Operating System:	SGI ProPack(TM) v3.0 Service Pack 1
Compiler:	Intel(R) C++ Compiler for Linux 8.1 (Build 20041021) MicroQuill SmartHeap Library 7.01 (www.microquill.com)
File System:	xfs
System State:	Single-user

Notes/Tuning Information

```
+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -fast -auto_ilp32 +FDO
  C++ programs:     -fast -ansi_alias +FDO
  Extra Libraries:  libsmartheap64.a

Portability Flags:
  176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca
  186.crafty: -DLINUX_i386
  252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST -DFMAX_IS_DOUBLE
  253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
  254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
           -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
  255.vortex: -DSPEC_CPU2000_LP64 srcalt=closed_files

Processes were bound to CPUs using dplace.
```