CINT2000 Result

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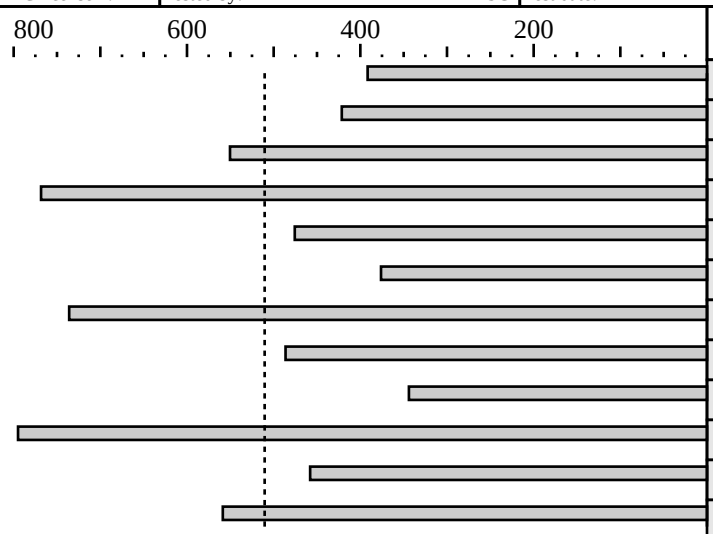
SGI

SGI Altix 3700 Bx2 (1600MHz 6M L3, Itanium 2)

SPECint_rate2000 = --

SPECint_rate_base2000 = 510

SPEC license #: 4 | Tested by: SGI | Test date: Oct-2004 | Hardware Avail: Nov-2004 | Software Avail: Nov-2004



Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	32	133	392			
175.vpr	32	123	421			
176.gcc	32	74.2	550			
181.mcf	32	87.0	768			
186.crafty	32	78.0	476			
197.parser	32	178	376			
252.eon	32	65.6	736			
253.perlbmk	32	137	486			
254.gap	32	119	344			
255.vortex	32	88.7	795			
256.bzip2	32	122	458			
300.twolf	32	199	559			

Hardware

CPU:	Intel Itanium 2
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	32 cores, 32 chips, 1 core/chip
CPU(s) orderable:	16-256
Parallel:	No
Primary Cache:	16KBI + 16KBD on chip, per core
Secondary Cache:	256KB (I+D) on chip, per core
L3 Cache:	6.0MB (I+D) on chip, per core
Other Cache:	N/A
Memory:	64 GB (32*512MB PC2700 DIMMS per 8 core module)
Disk Subsystem:	8 x 73 GB FibreChannel (Seagate Cheetah 10k rpm)
Other Hardware:	None

Software

Operating System:	SGI ProPack(TM) v3.0 Service Pack 2
Compiler:	Intel(R) C++ Compiler for Linux 8.1 (Build 20041021) MicroQuill SmartHeap Library 7.01 (www.microquill.com)
File System:	xfs
System State:	Single-user

Notes/Tuning Information

```
+FDO: PASS1=-prof_gen  PASS2=-prof_use

Baseline optimization flags:
  C programs:       -fast -auto_ilp32 +FDO
  C++ programs:     -fast -ansi_alias +FDO
  Extra Libraries:  libsmartheap64.a

Portability Flags:
  176.gcc: -DSPEC_CPU2000_LP64 -Dalloca=_builtin_alloca
  186.crafty: -DLINUX_i386
  252.eon: -DSPEC_CPU2000_LP64 -DHAS_ERRLIST -DFMAX_IS_DOUBLE
  253.perlbmk: -DSPEC_CPU2000_LP64 -DSPEC_CPU2000_NEED_BOOL
               -DSPEC_CPU2000_LINUX_IA64 -DSPEC_CPU2000_GLIBC22
  254.gap: -DSPEC_CPU2000_LP64 -DSYS_HAS_CALLOC_PROTO -DSYS_IS_USG
           -DSYS_HAS_IOCTL_PROTO -DSYS_HAS_TIME_PROTO -DSYS_HAS_SIGNAL_PROTO
  255.vortex: -DSPEC_CPU2000_LP64 srcalt=closed_files

Processes were bound to CPUs using dplace.
```