# CINT2000 Result

Copyright ©1999-2004, Standard Performance Evaluation Corporation

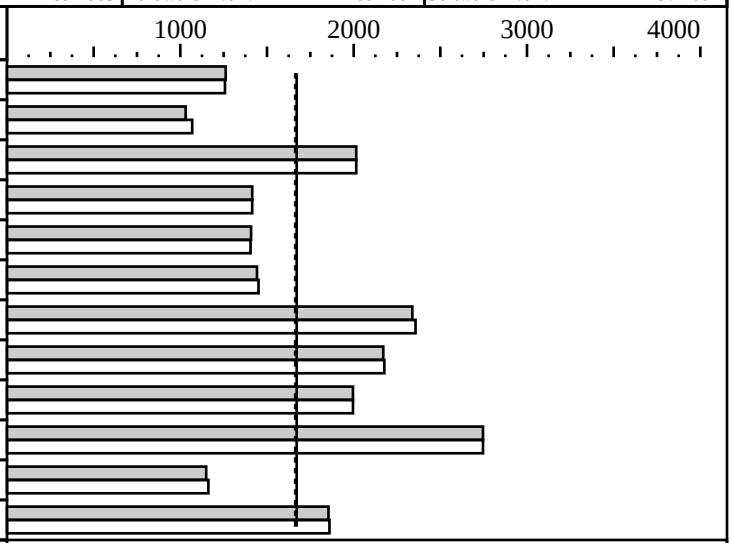
## Fujitsu Siemens Computers
## CELSIUS M430, Pentium 4 3.8 GHz

SPECint2000 = 1671

SPECint_base2000 = 1663

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Feb-2005 | Hardware Avail: Dec-2004 | Software Avail: Nov-2004

| Benchmark | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio |
|---|---|---|---|---|---|
| 164.gzip | 1400 | 111 | 1262 | 111 | 1258 |
| 175.vpr | 1400 | 136 | 1031 | 131 | 1068 |
| 176.gcc | 1100 | 54.6 | 2015 | 54.6 | 2015 |
| 181.mcf | 1800 | 127 | 1415 | 127 | 1415 |
| 186.crafty | 1000 | 71.0 | 1408 | 71.2 | 1405 |
| 197.parser | 1800 | 125 | 1442 | 124 | 1451 |
| 252.eon | 1300 | 55.6 | 2338 | 55.2 | 2357 |
| 253.perlbmk | 1800 | 82.9 | 2171 | 82.7 | 2177 |
| 254.gap | 1100 | 55.1 | 1996 | 55.1 | 1996 |
| 255.vortex | 1900 | 69.2 | 2746 | 69.2 | 2746 |
| 256.bzip2 | 1500 | 131 | 1149 | 129 | 1162 |
| 300.twolf | 3000 | 162 | 1855 | 161 | 1860 |




### Hardware

| | |
|---|---|
| CPU: | Intel Pentium 4 (3.8 GHz, 800 MHz bus) |
| CPU MHz: | 3800 |
| FPU: | Integrated |
| CPU(s) enabled: | 1 core, 1 chip, 1 core/chip (Hyper-Threading Technology disabled) |
| CPU(s) orderable: | 1 |
| Parallel: | No |
| Primary Cache: | 12k micro-ops I + 16KBD on chip |
| Secondary Cache: | 1024KB(I+D) on chip |
| L3 Cache: | N/A |
| Other Cache: | N/A |
| Memory: | 2 GB (4x512 MB DDRII 533, CL4-4-4, with ECC) |
| Disk Subsystem: | Maxtor 6Y160M0 (Serial ATA 7200 rpm) |
| Other Hardware: | |

### Software

| | |
|---|---|
| Operating System: | Windows XP Professional, Service Pack 1 |
| Compiler: | Intel(R) C++ Compiler, Version 8.1, - Build 20041019Z, Package ID: W_CC_PU_8.1.020<br>Microsoft Visual Studio .NET 7.0 (for libraries)<br>MicroQuill SmartHeap Library 6.03a |
| File System: | FAT32 |
| System State: | Default |

## Notes/Tuning Information

```
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
253.perlbmk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Feedback optimization:
  +FDO: PASS1= -Qprof_gen PASS2= -Qprof_use

 Baseline Tuning Flags:
 for C programs (except 252.eon):
 -fast +FDO shlW32M.lib
 for C++ program 252.eon:
 -fast -Qcxx_features +FDO shlW32M.lib

 Peak Tuning Flags:
 164.gzip:     -fast -Qansi_alias -Oa +FDO
 175.vpr:      -fast -Qansi_alias +FDO
 176.gcc:      basepeak=yes
 181.mcf:      basepeak=yes
```

Copyright ©1999-2004, Standard Performance Evaluation Corporation



## Notes/Tuning Information (Continued)

```
 186.crafty:   -fast -Qansi_alias -Oa +FDO
 197.parser:   -fast -Qansi_alias +FDO
 252.eon:      -fast +FDO
 253.perlbmk:  -fast -Qansi_alias +FDO shlW32M.lib
 254.gap:      basepeak=yes
 255.vortex    basepeak=yes
 256.bzip2:   -fast -Oa -Qunroll1 +FDO
 300.twolf:    -fast -O3 +FDO shlW32M.lib

Extra Libraries:
 shlW32M.lib = Microquill SmartHeap Library 6.03a
 see www.microquill.com

The system bus runs at 800 MHz

For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
```